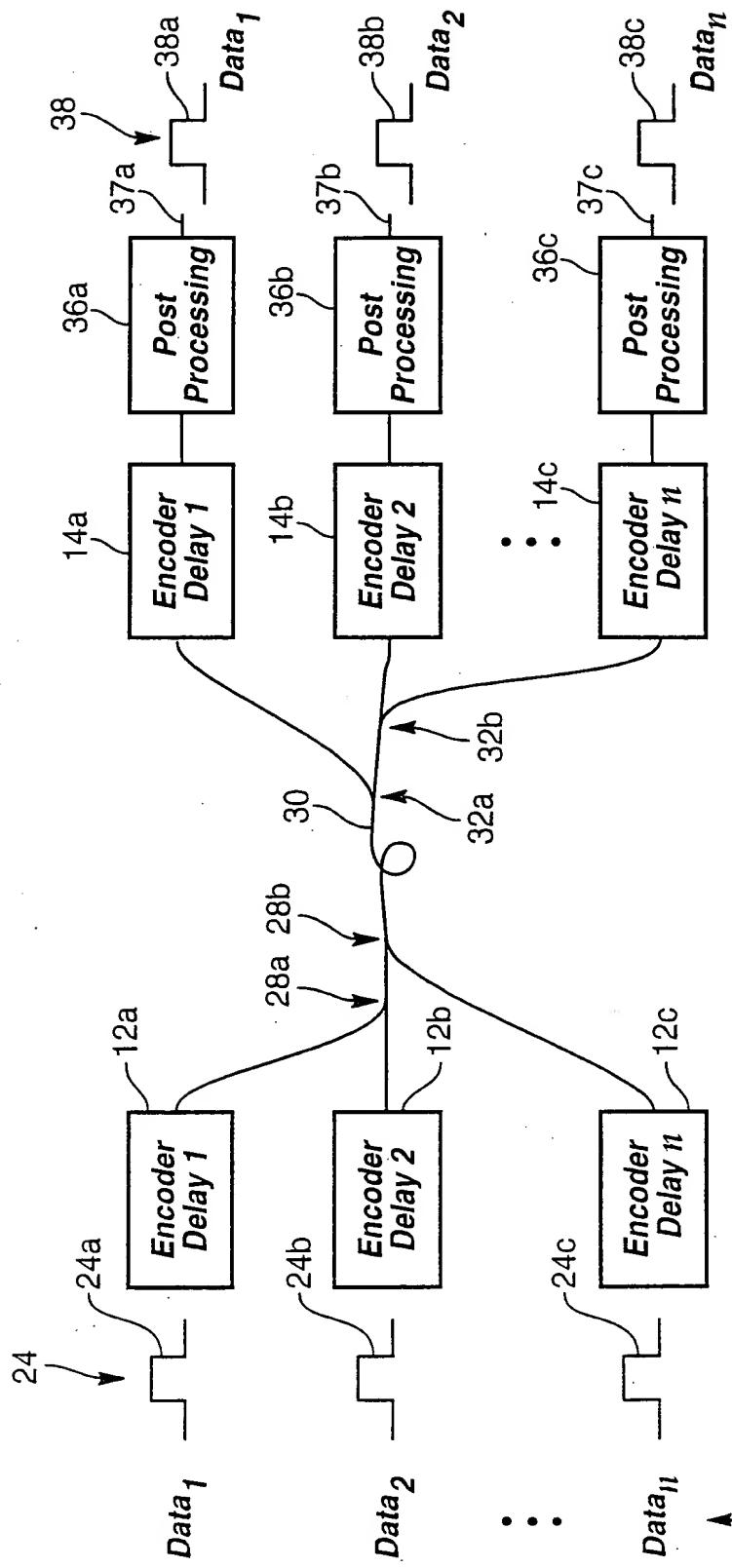


**FIG. 1**

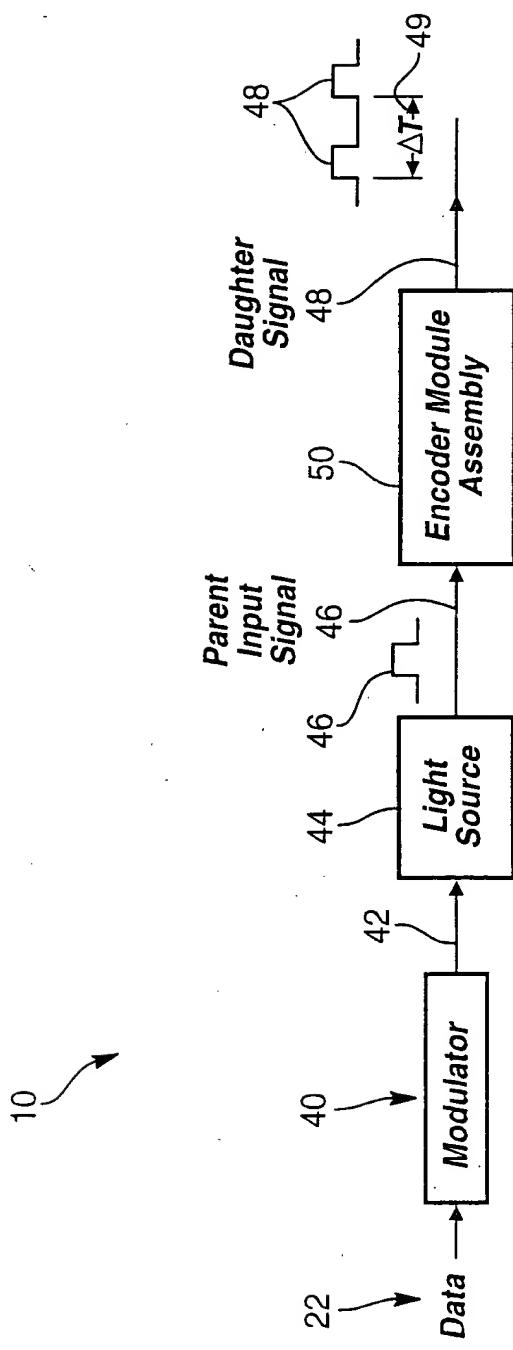
*Photonic Information  
Digital or Analog*

10

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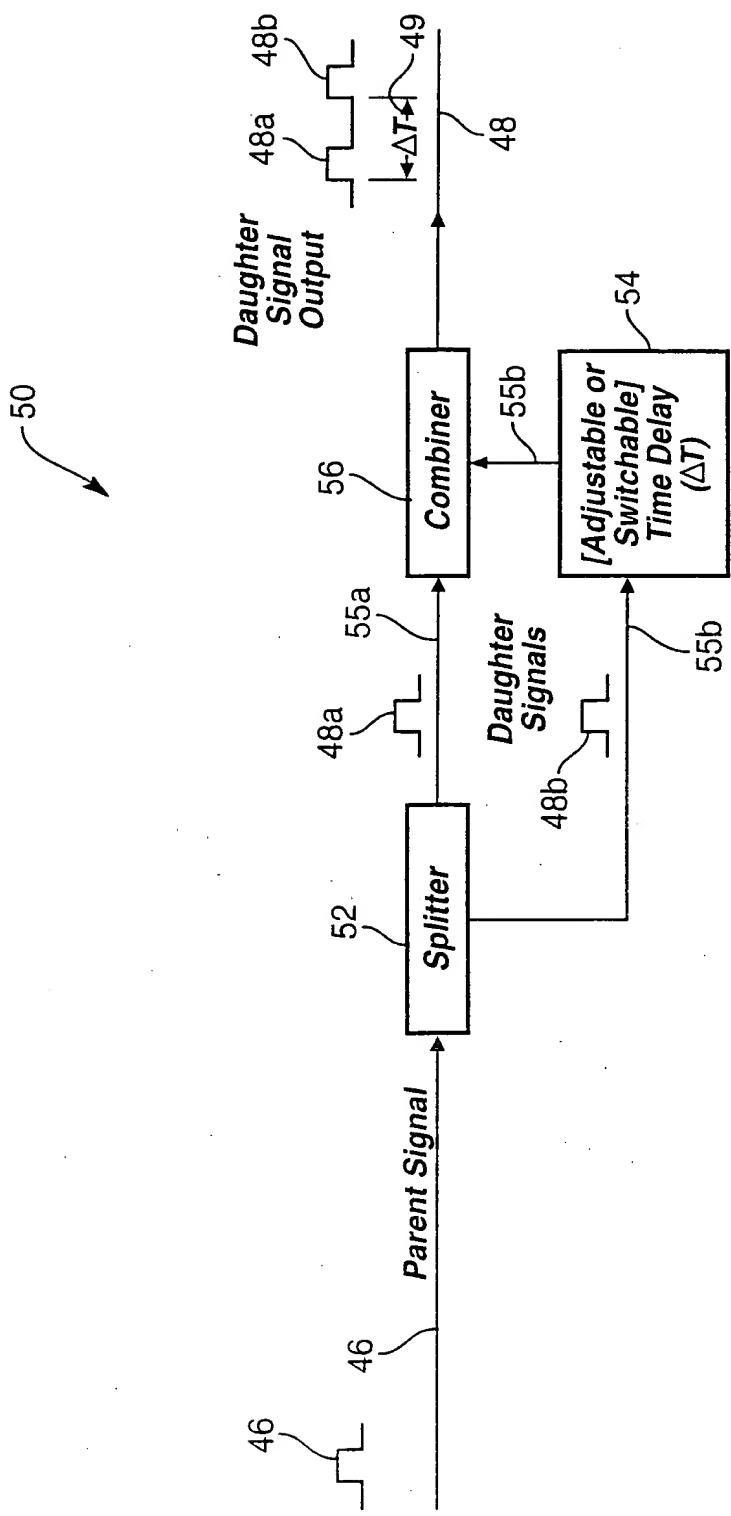


**Fig. 2**



Differential Delay Multiplexer Sender/Encoder

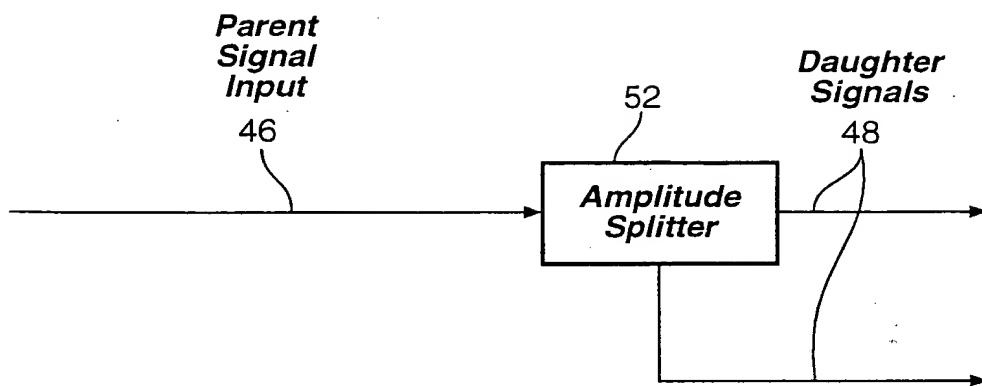
*Fig. 3*



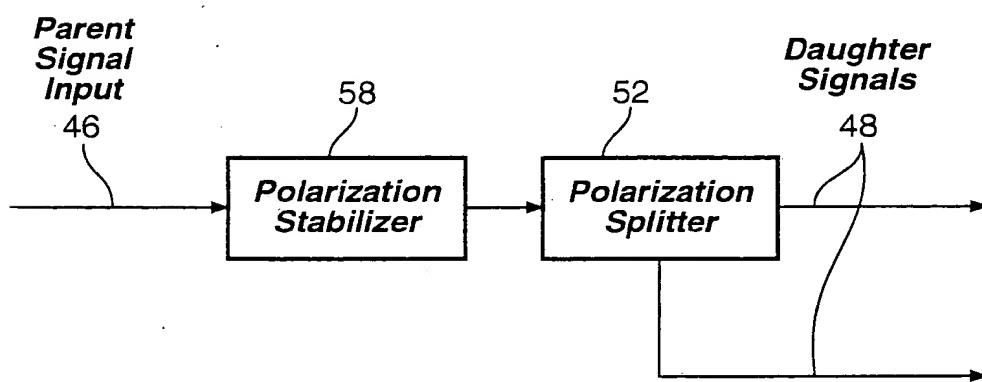
Differential Delay Multiplexer (DDM) Sender/Encoder

Fig. 4

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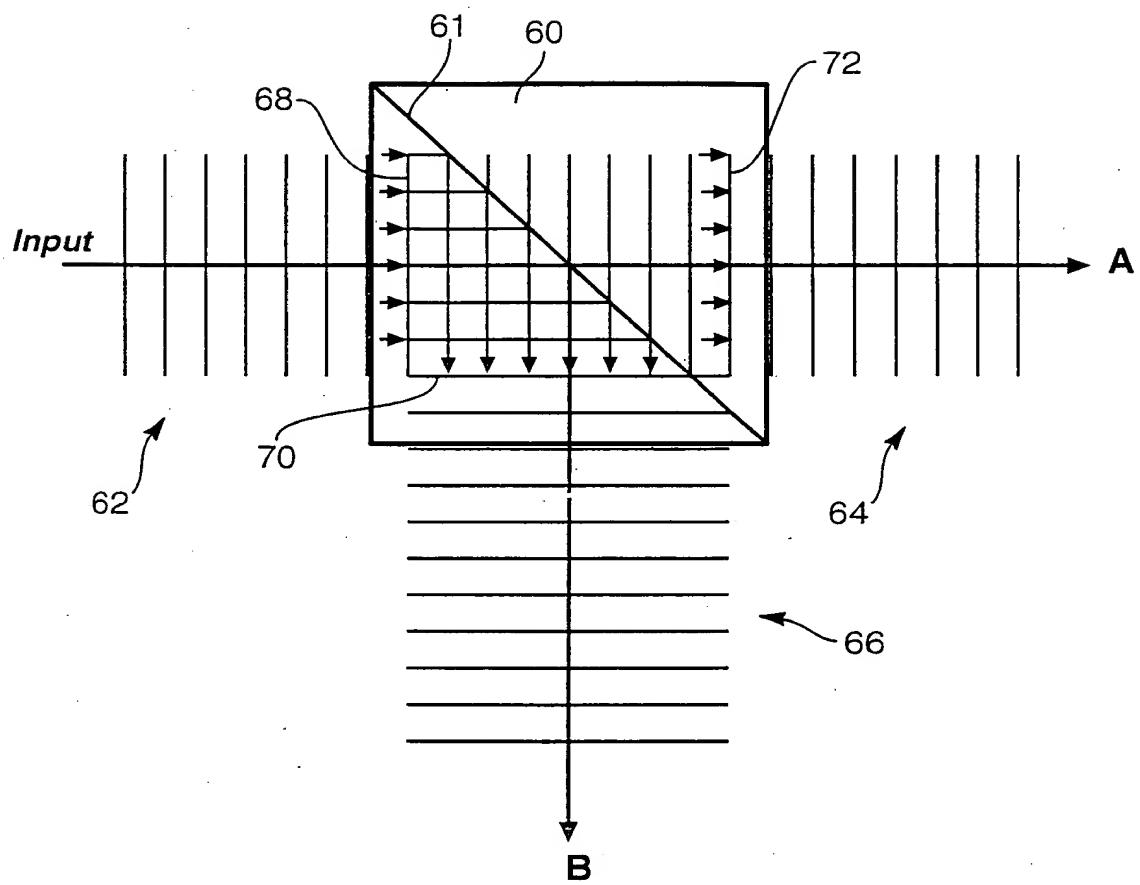


**FIG. 5**



**Splitting Criteria**

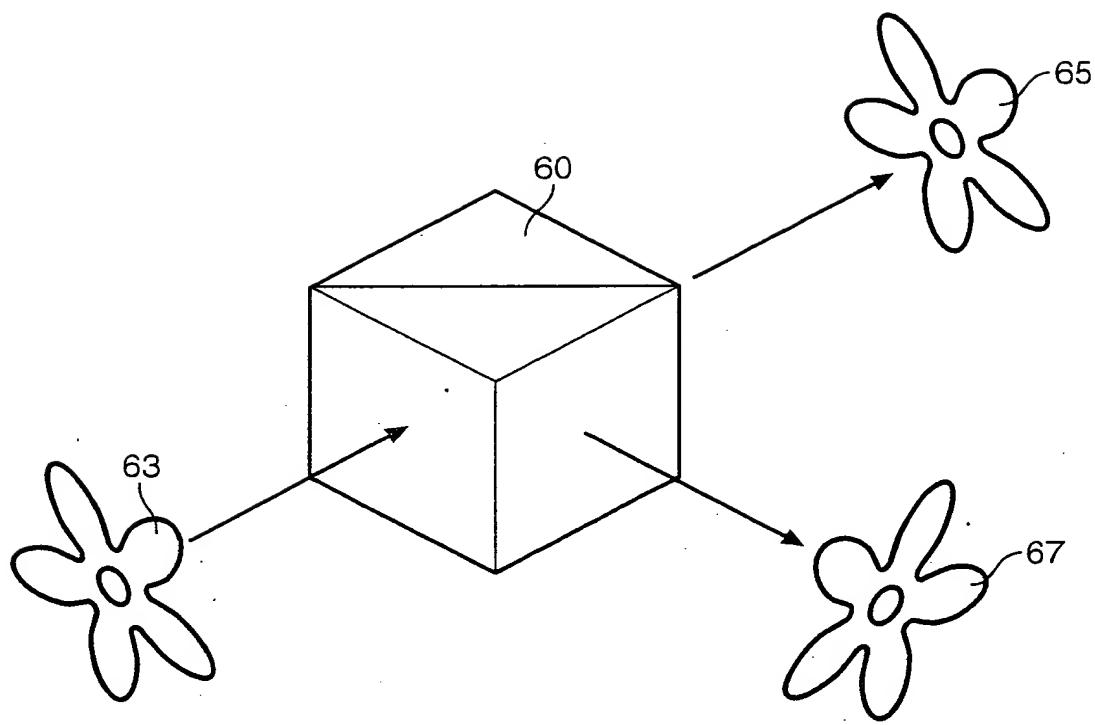
**FIG. 6**



Amplitude or Polarization Splitter

**FIG. 7**

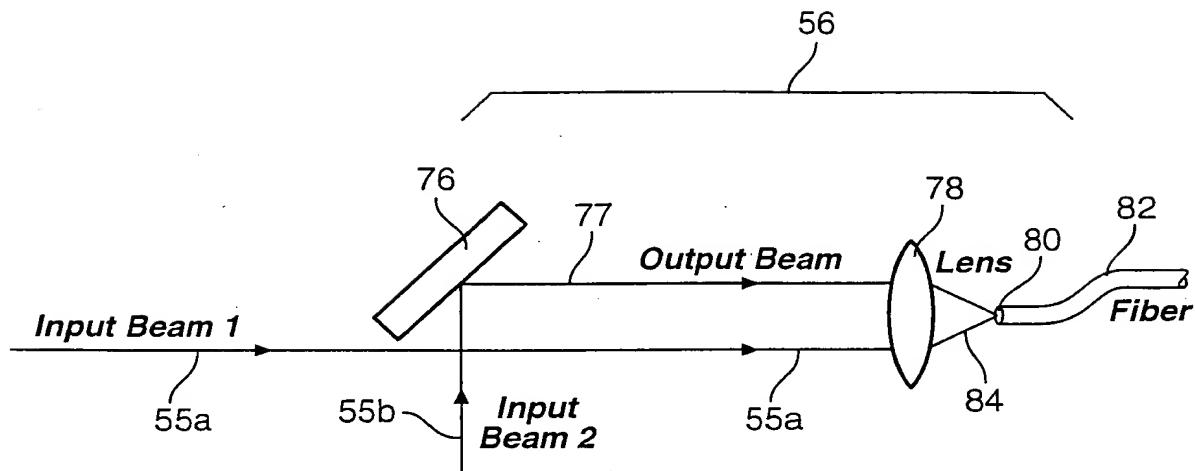
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**Image Signals Which  
Maintain Spatial Information**

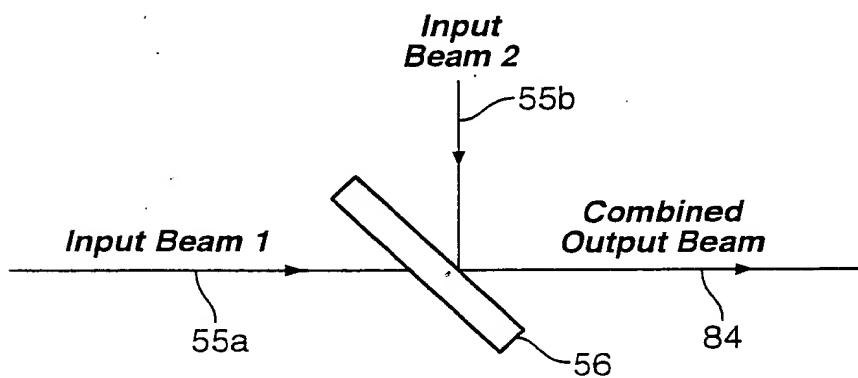
**FIG. 7A**

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Beam Combiner

**FIG. 8**



Amplitude or Polarization Combiner

**FIG. 9**

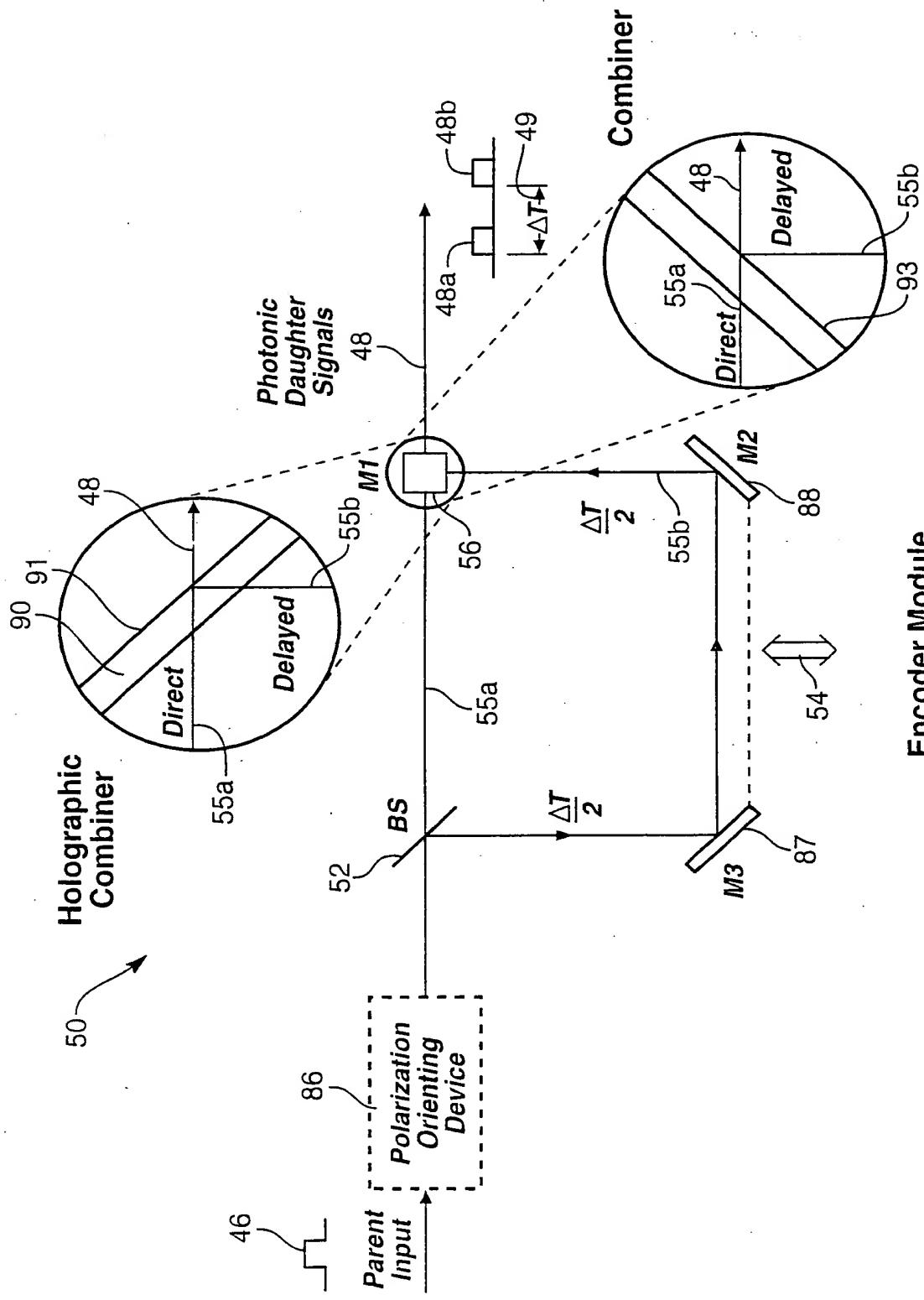
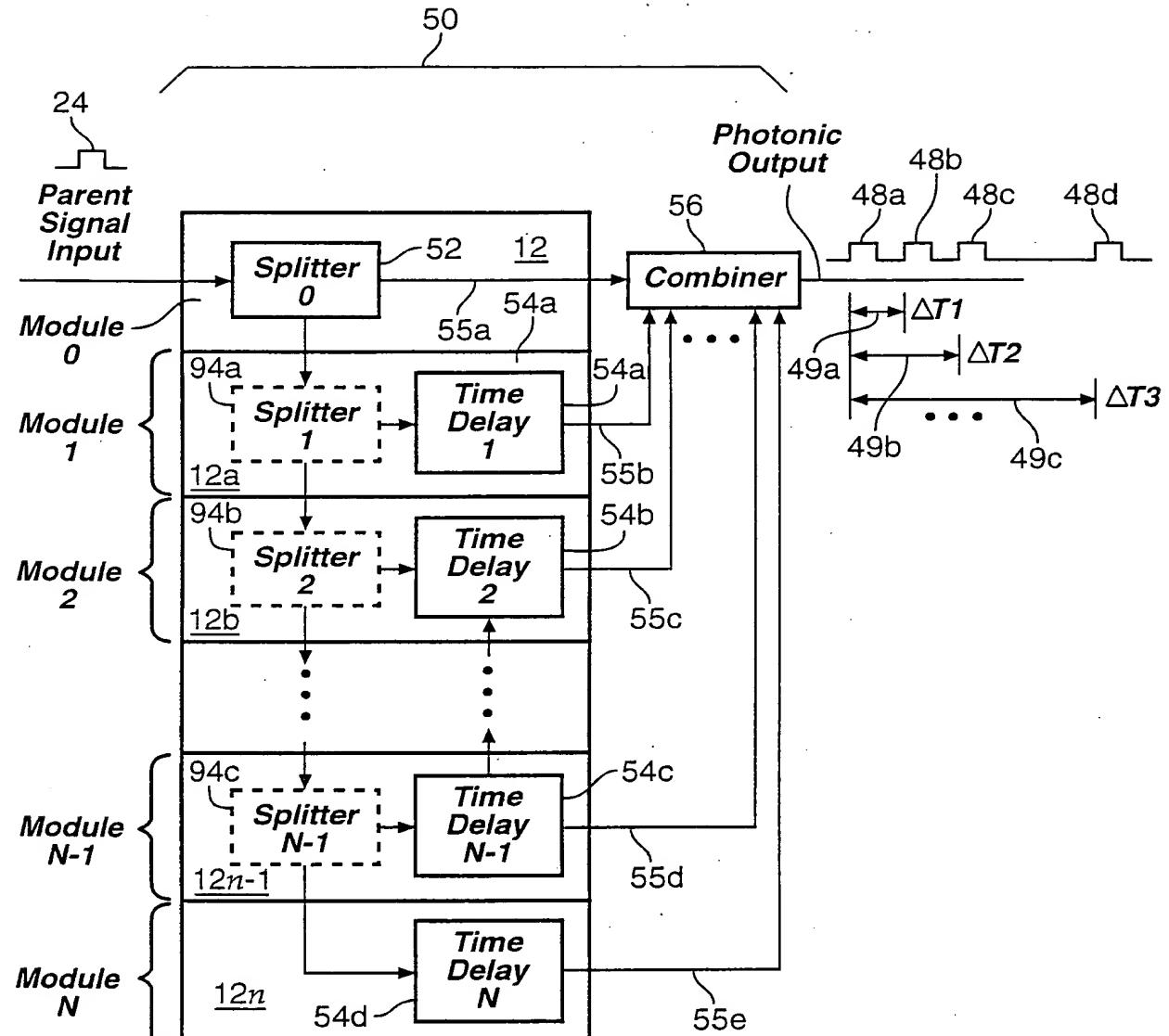


Fig. 10

Encoder Module



Composite Encoder  
Module Assembly

FIG. 11

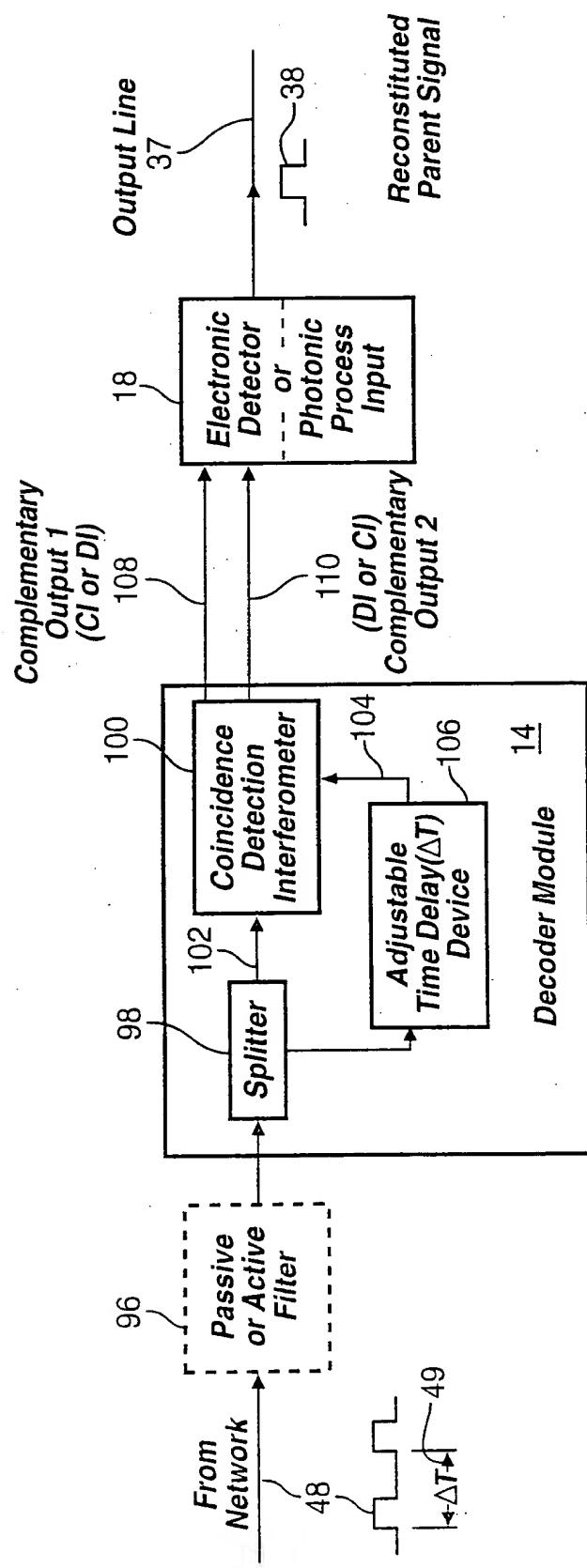
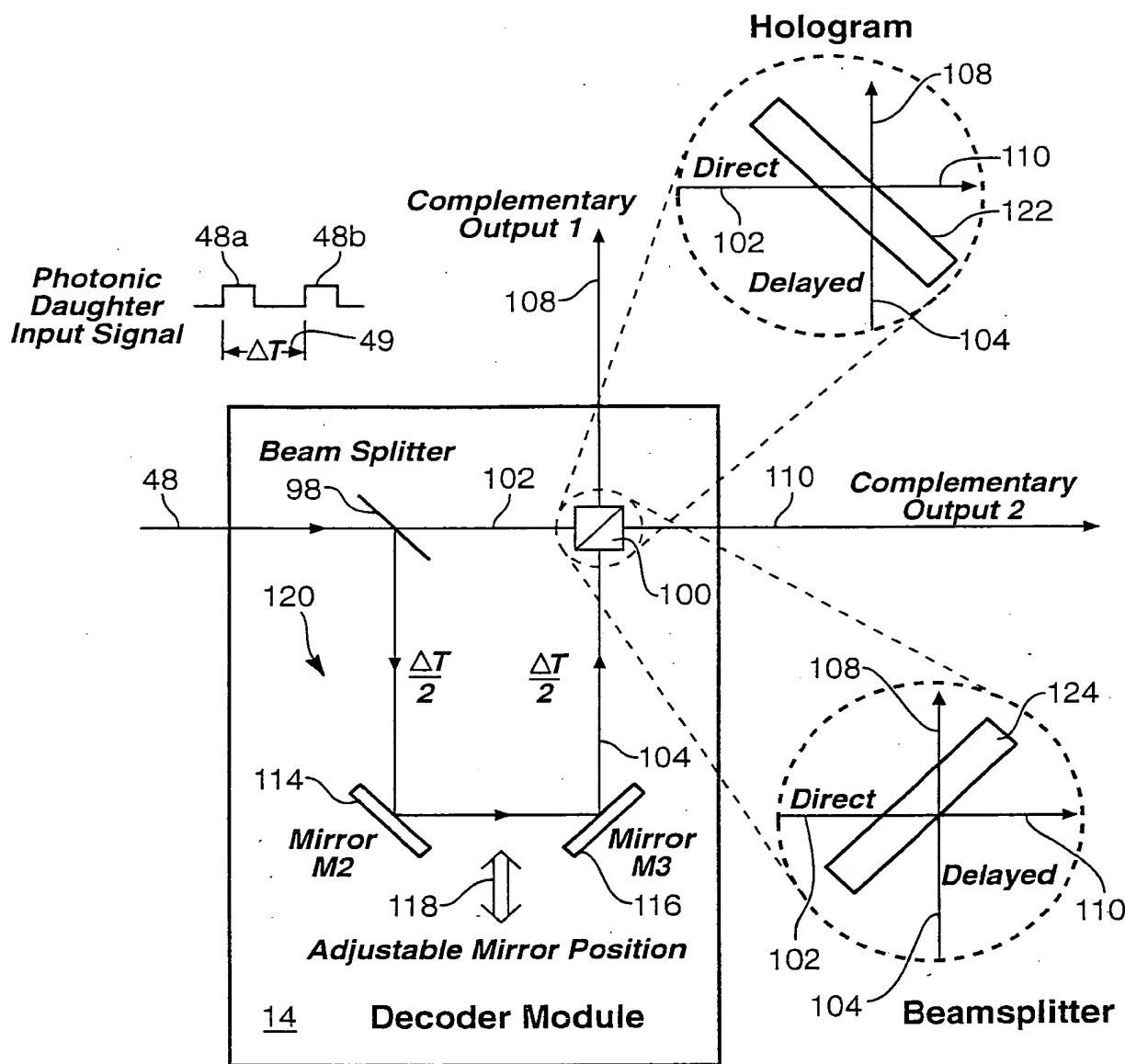


Fig. 12

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**FIG. 13**

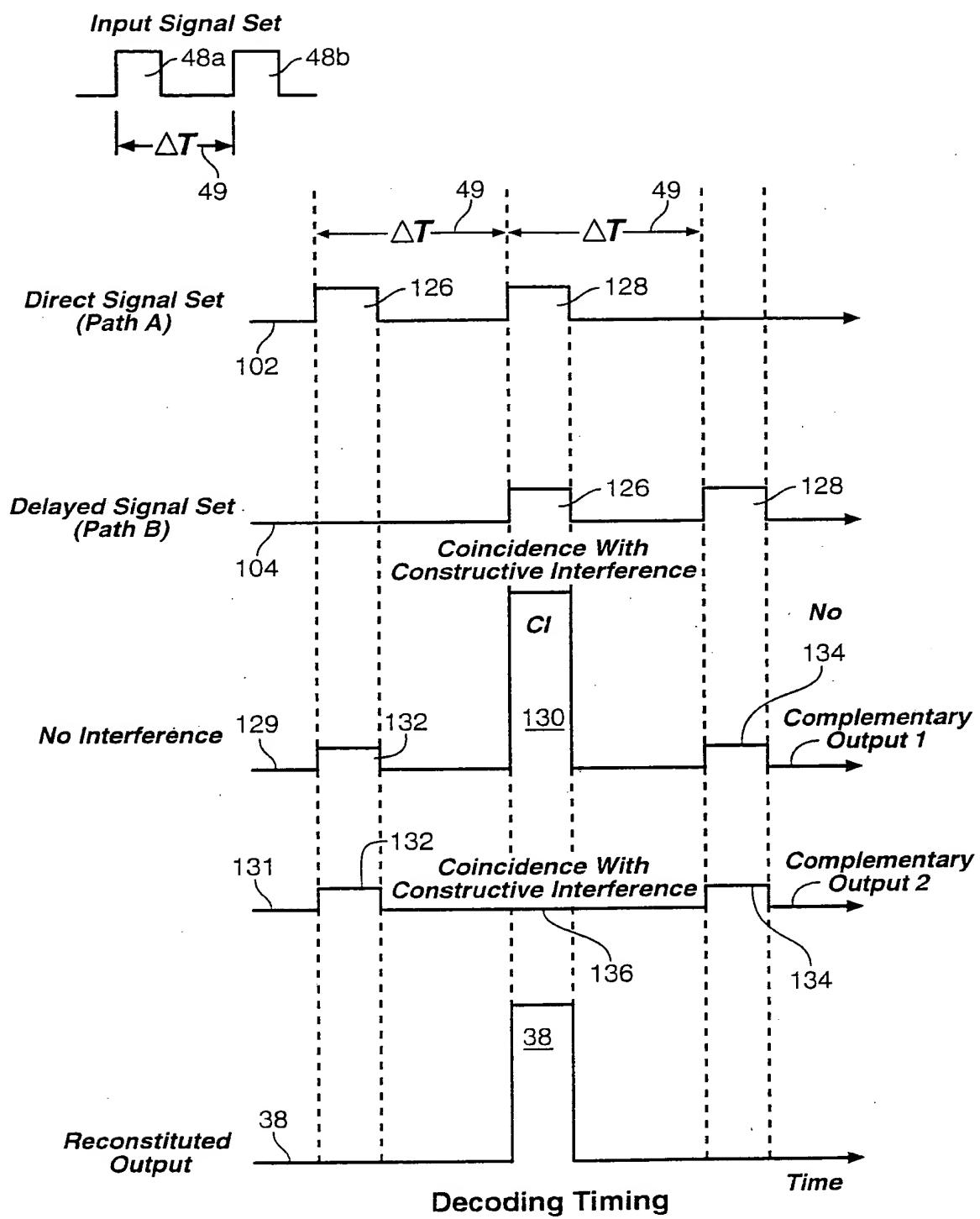


FIG. 14

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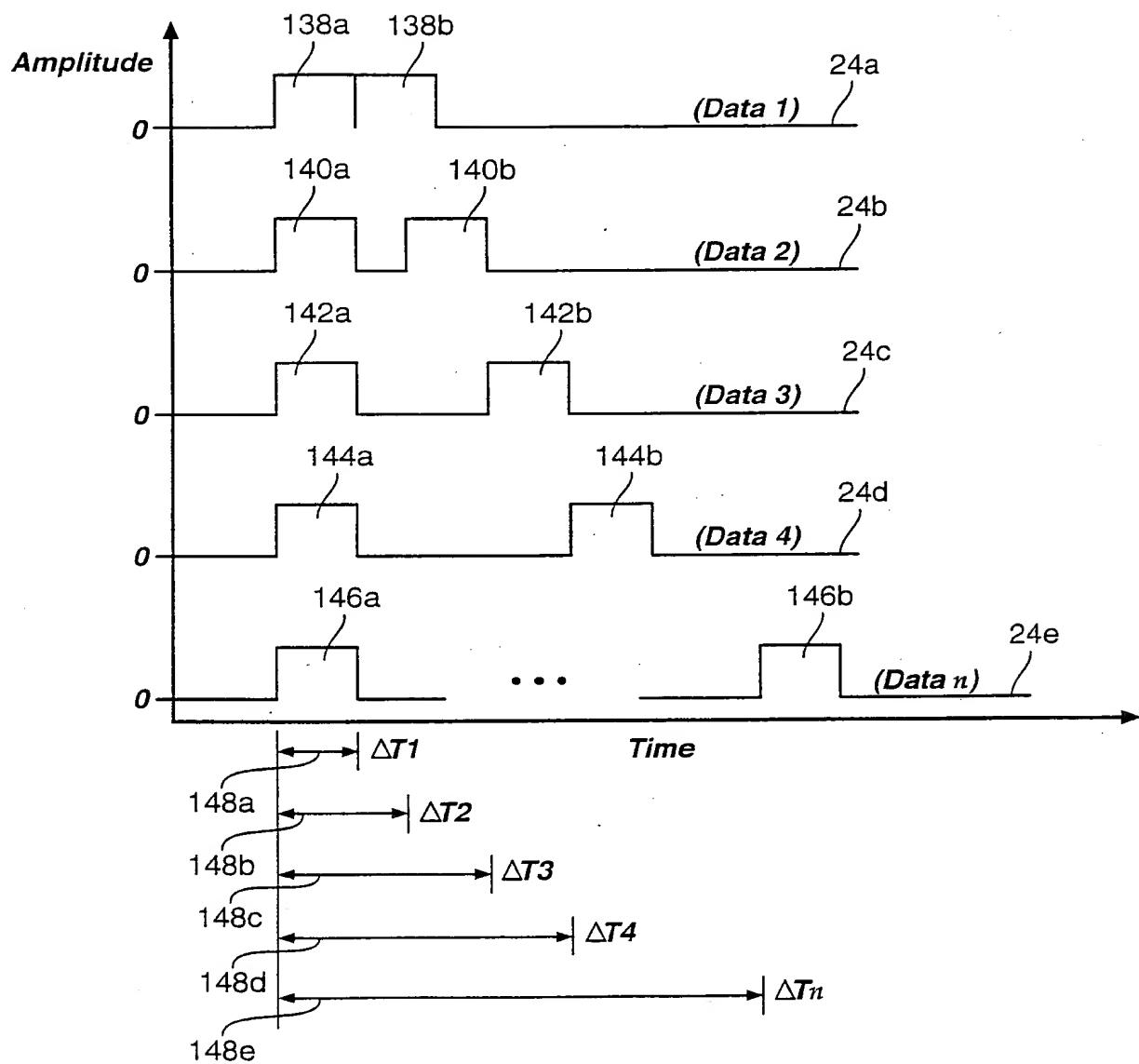


Fig. 15

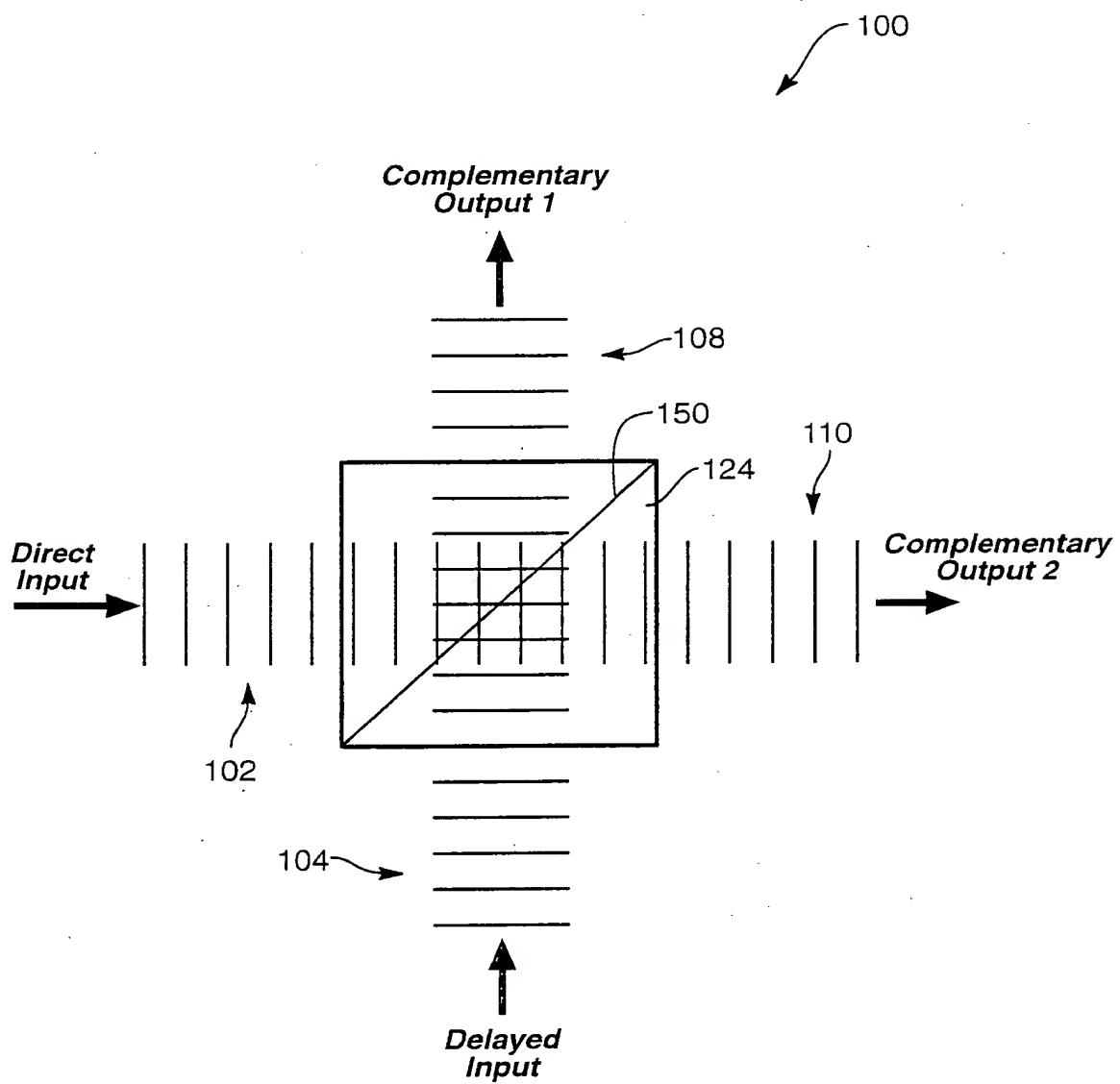


Fig. 16

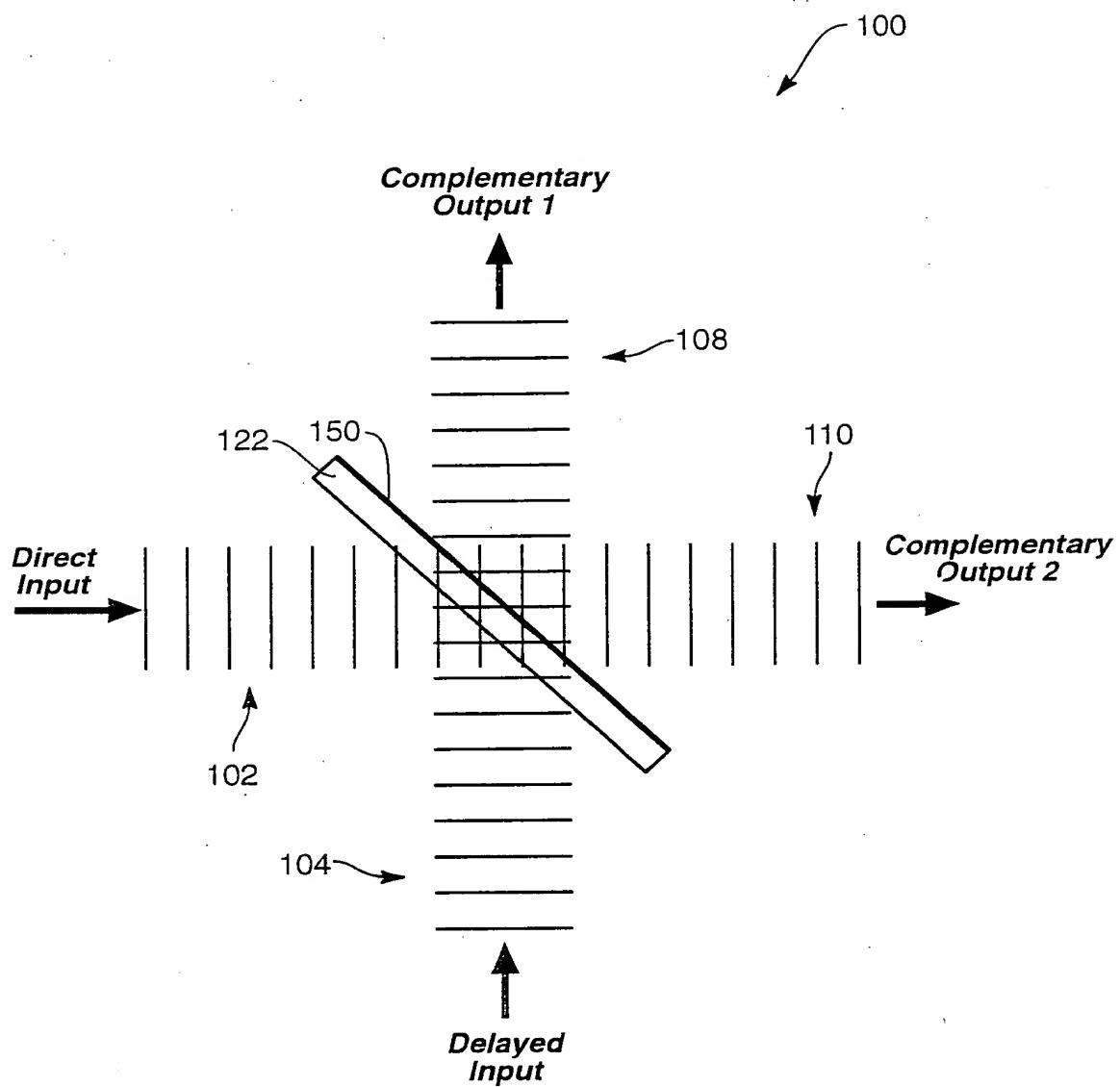


Fig. 17

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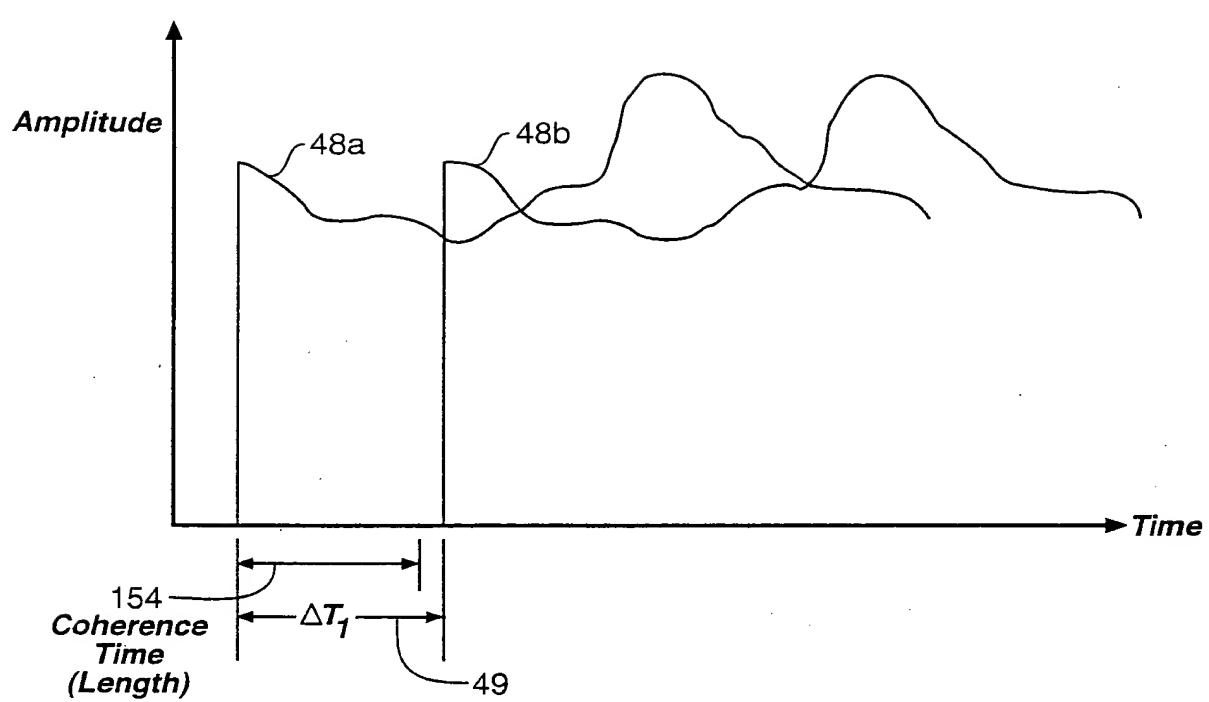


Fig. 18

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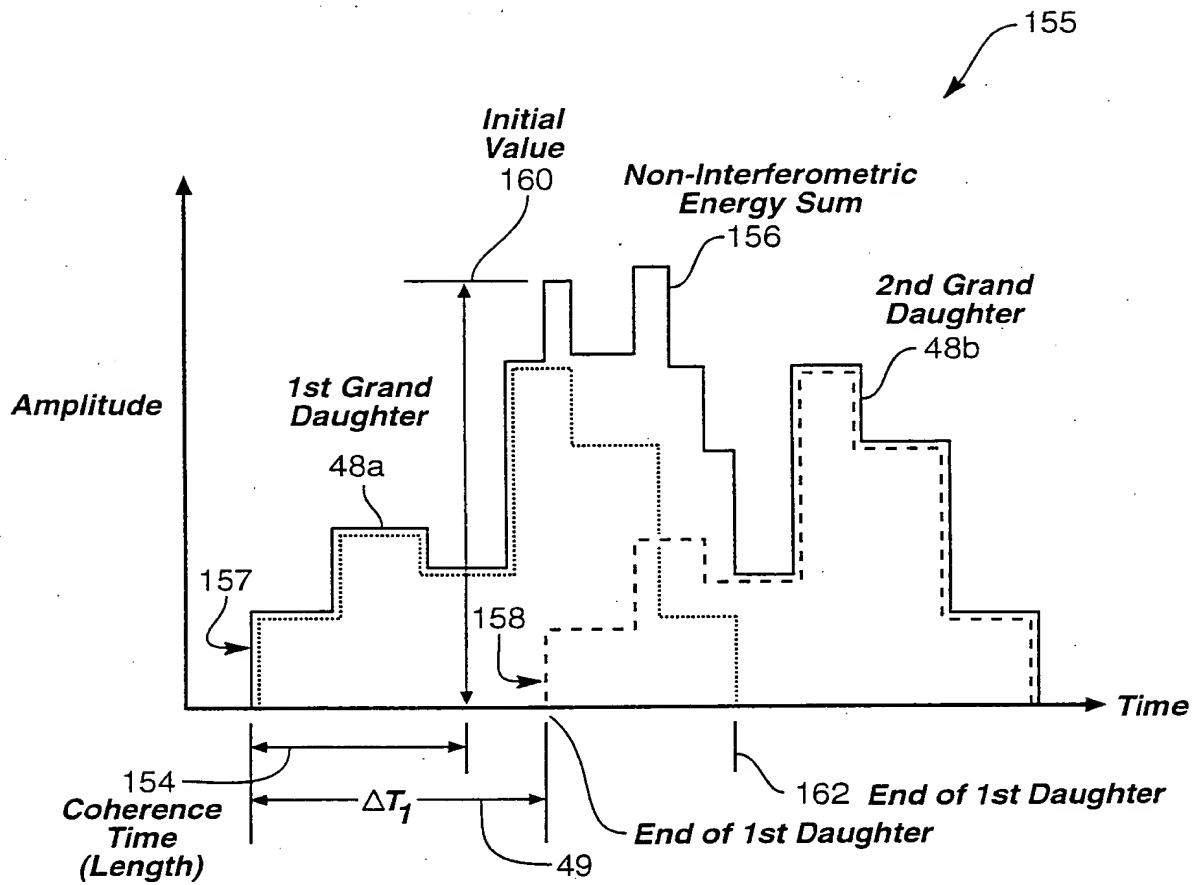
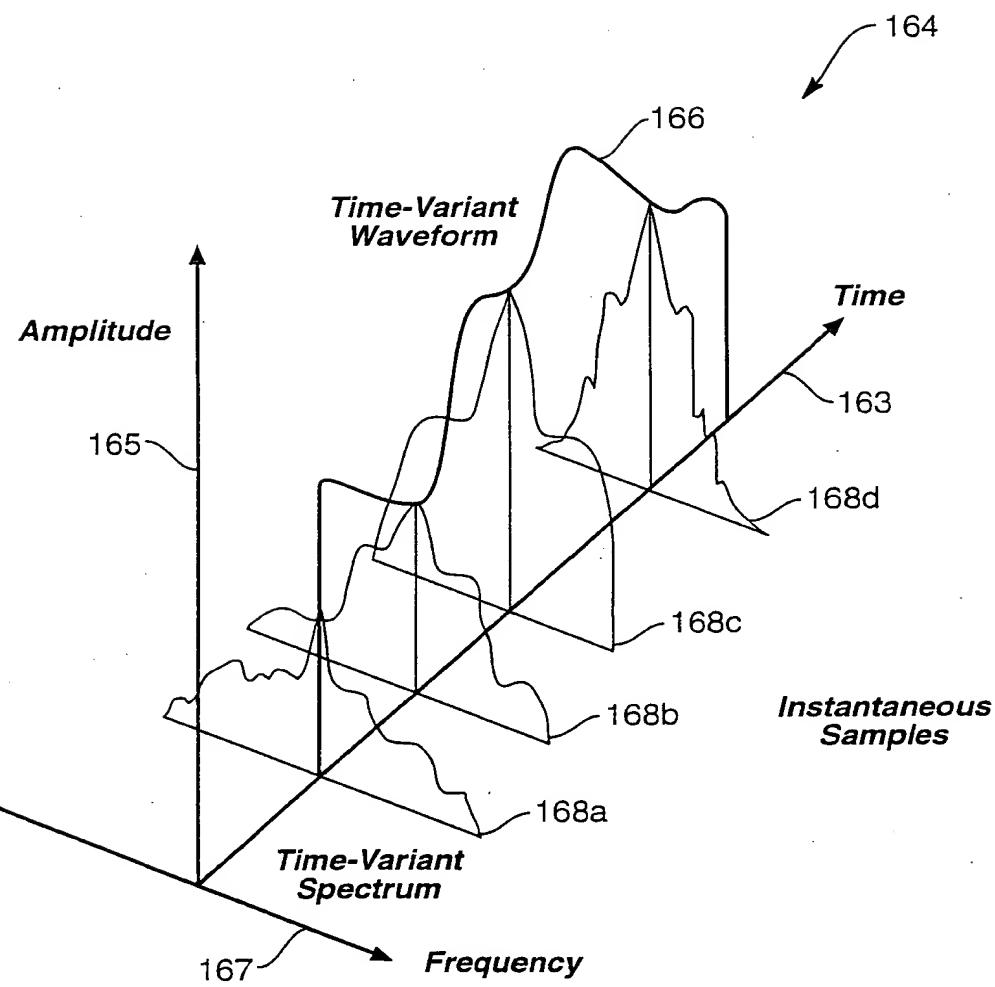


Fig. 19



*Fig. 20*

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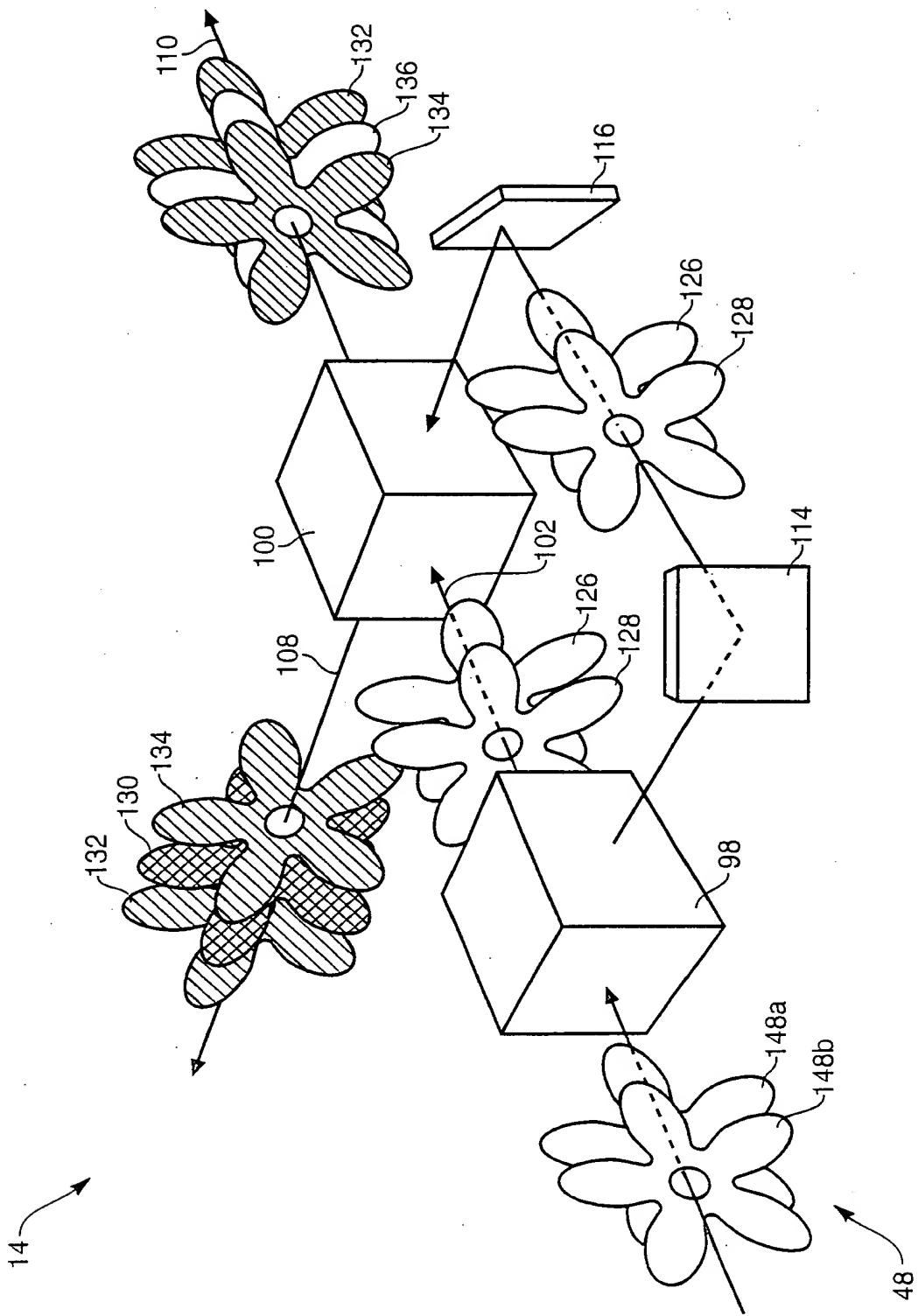
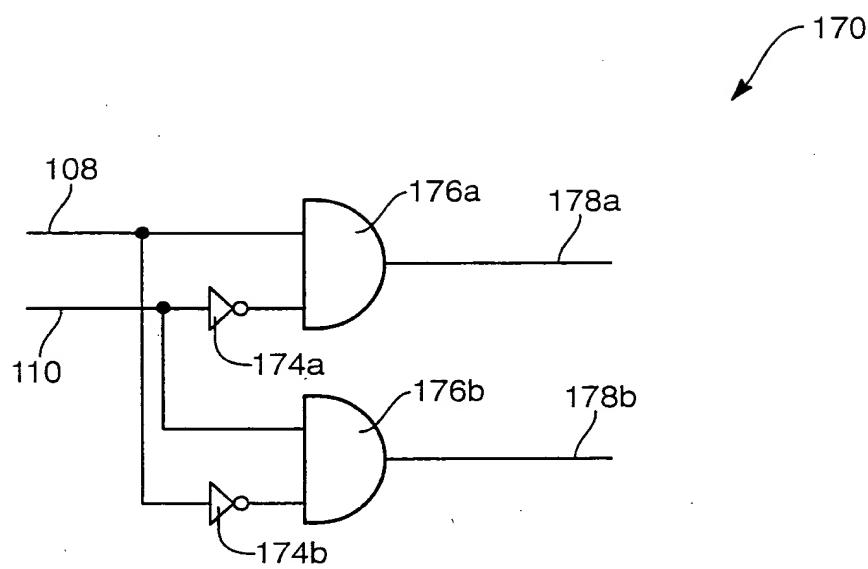


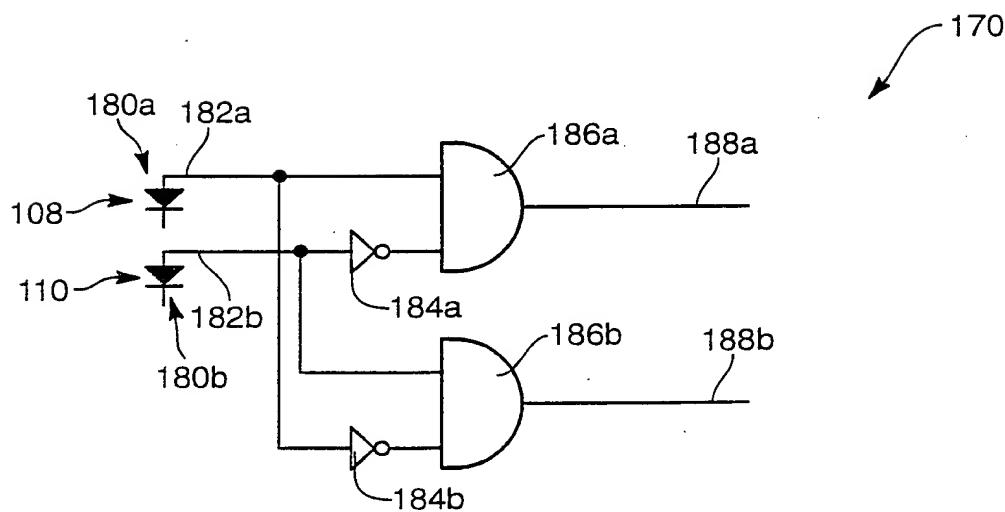
Fig. 21

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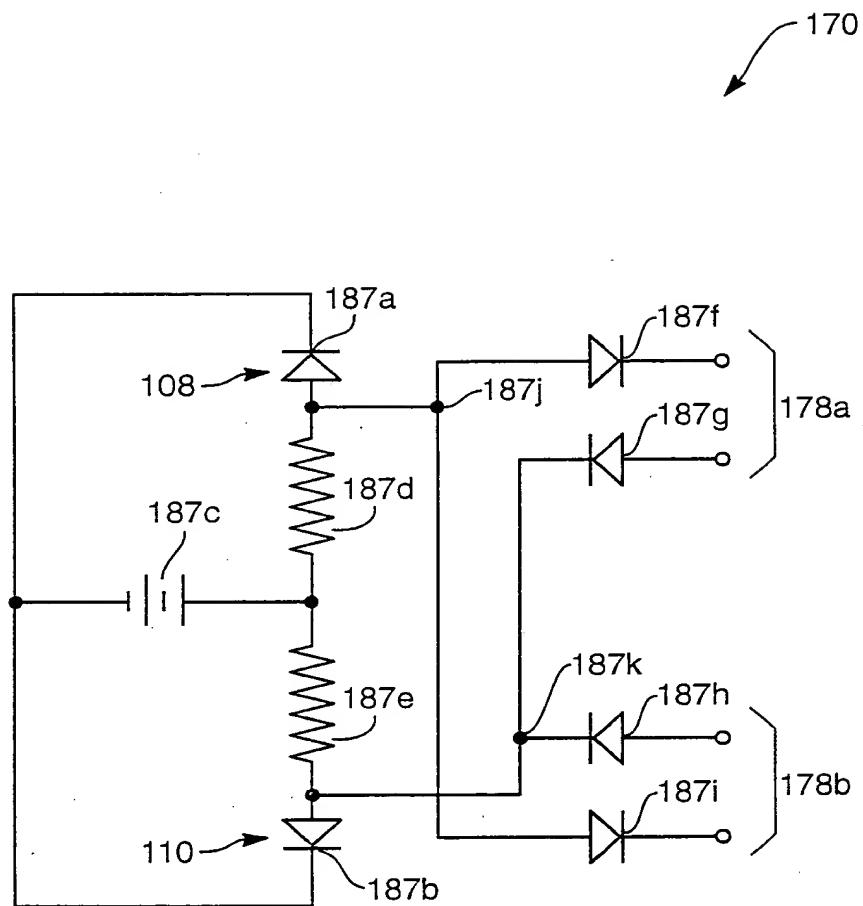
Photonic Processor

*Fig. 22*



Electronic Processor

*Fig. 23A*



Electronic Processor

*Fig. 23B*

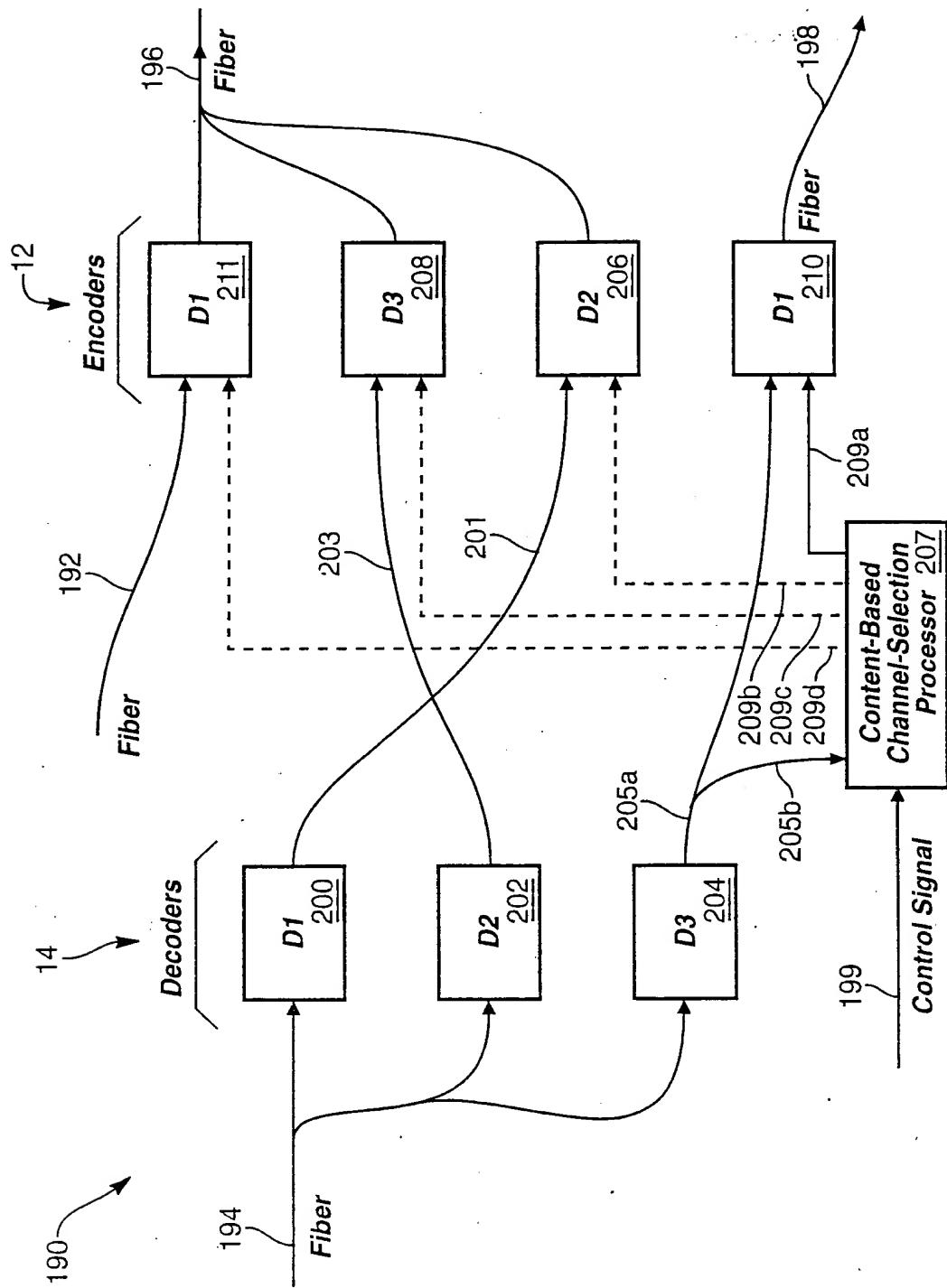
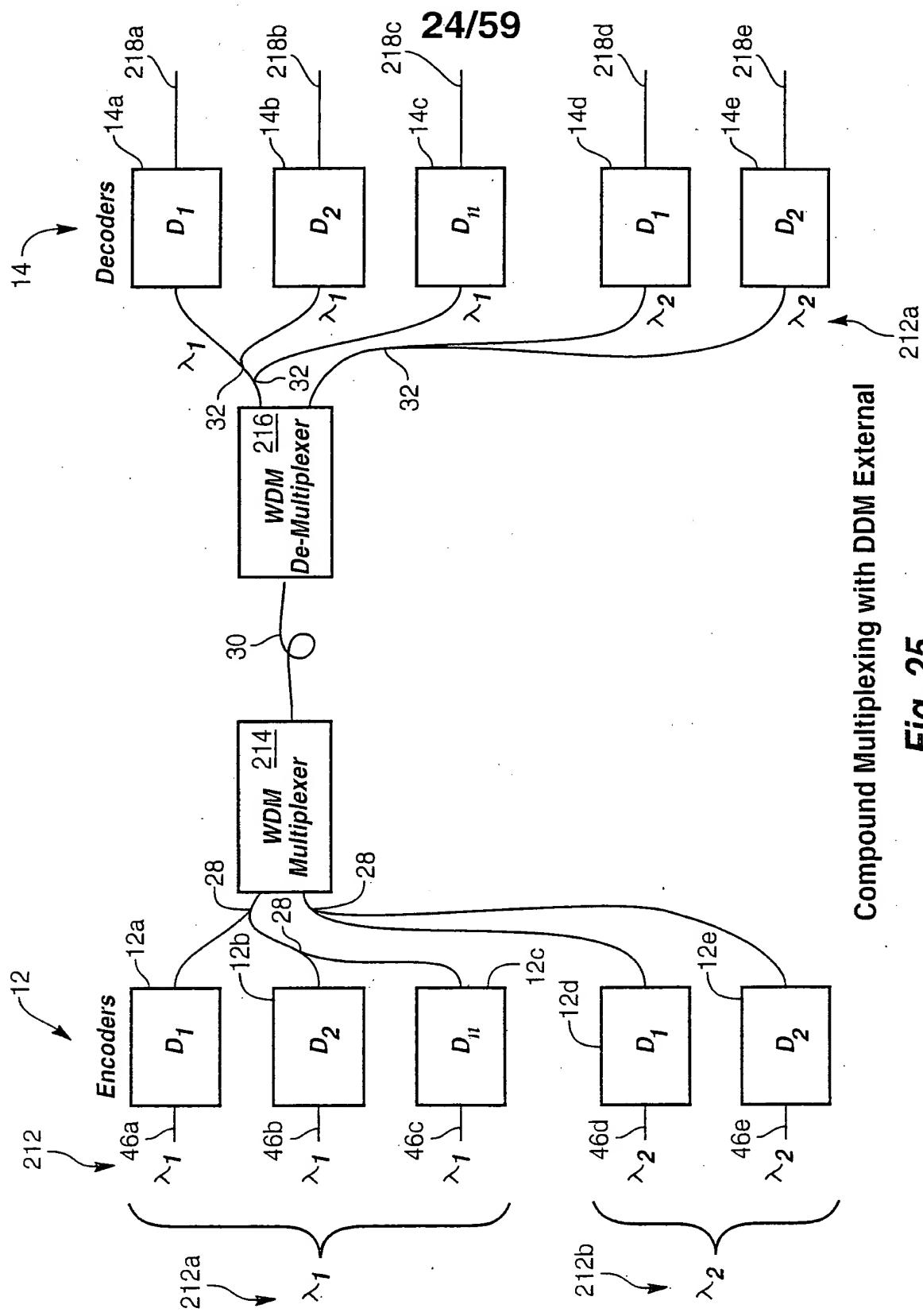
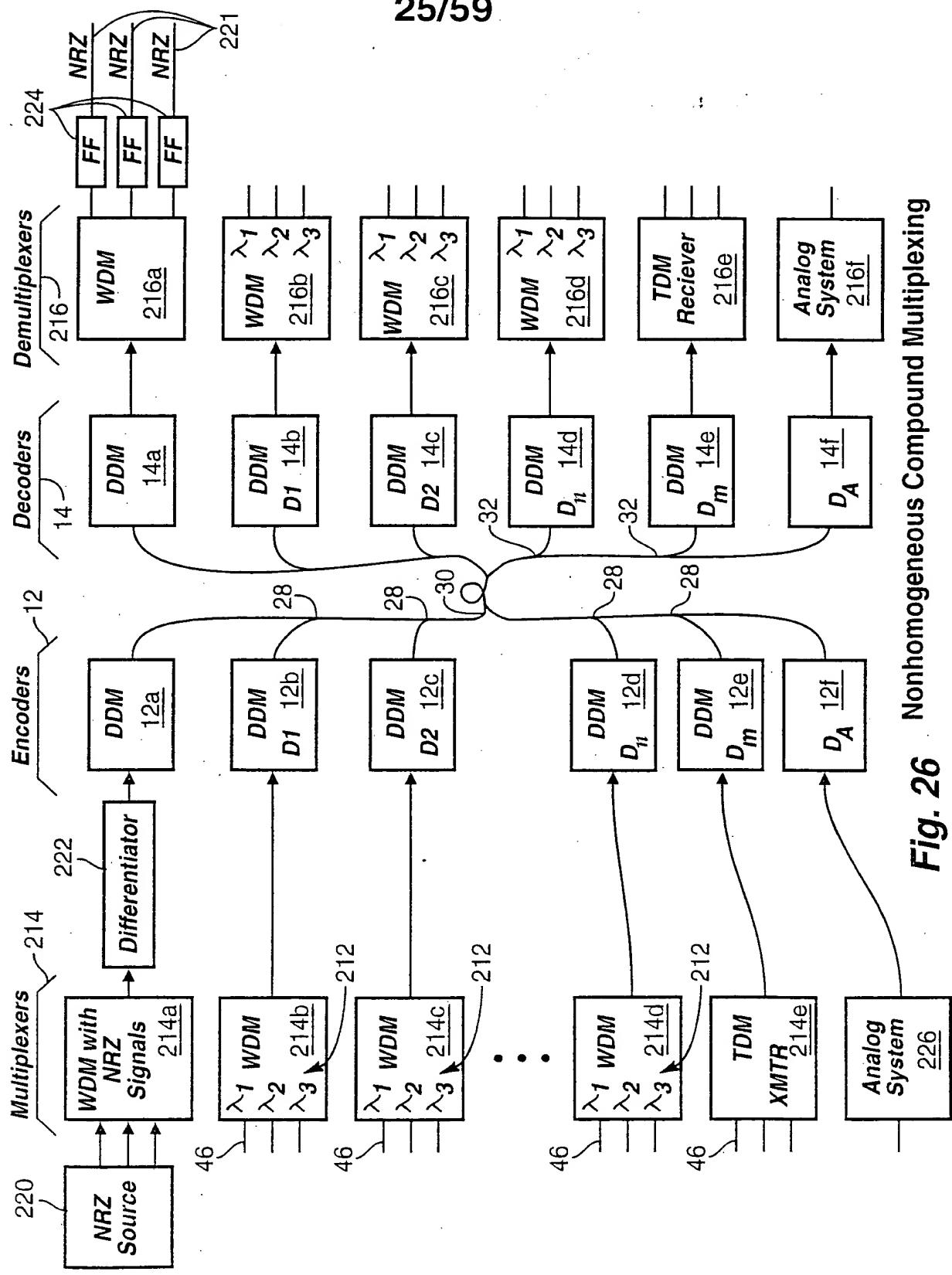
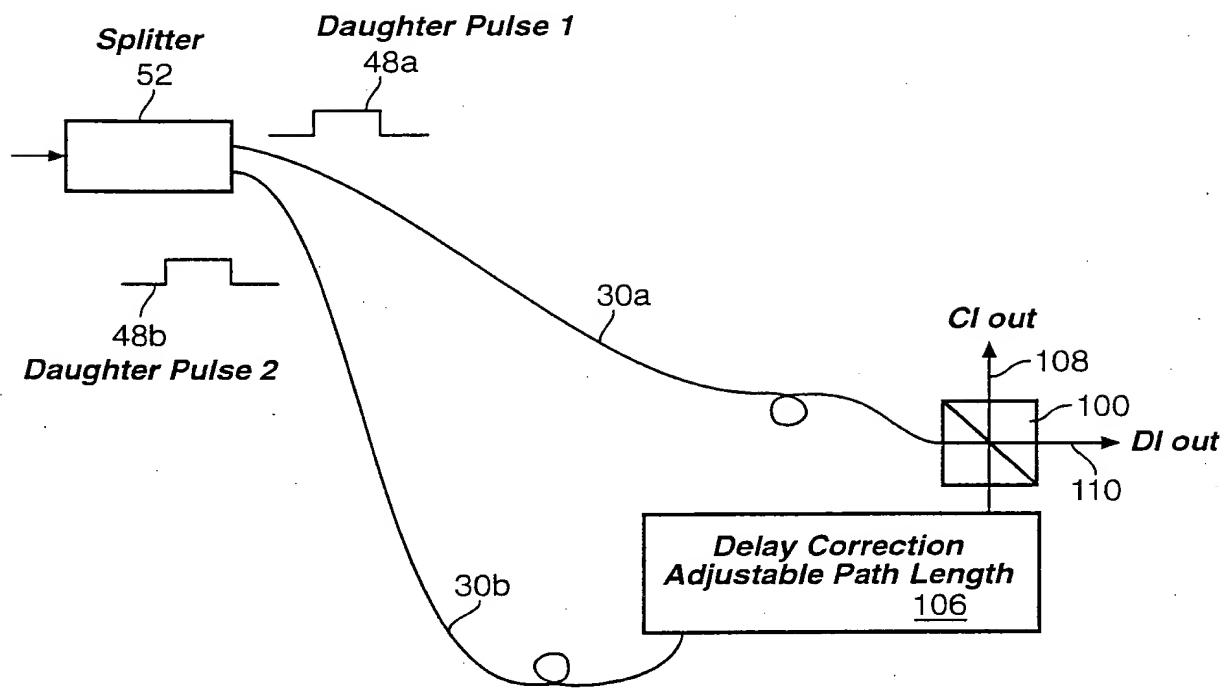


Fig. 24





**Fig. 26 Nonhomogeneous Compound Multiplexing**



Multiple Delay Path

Integrated Delay and  
Delay Correction

**FIG. 27**

### Photonic NRZ Interface

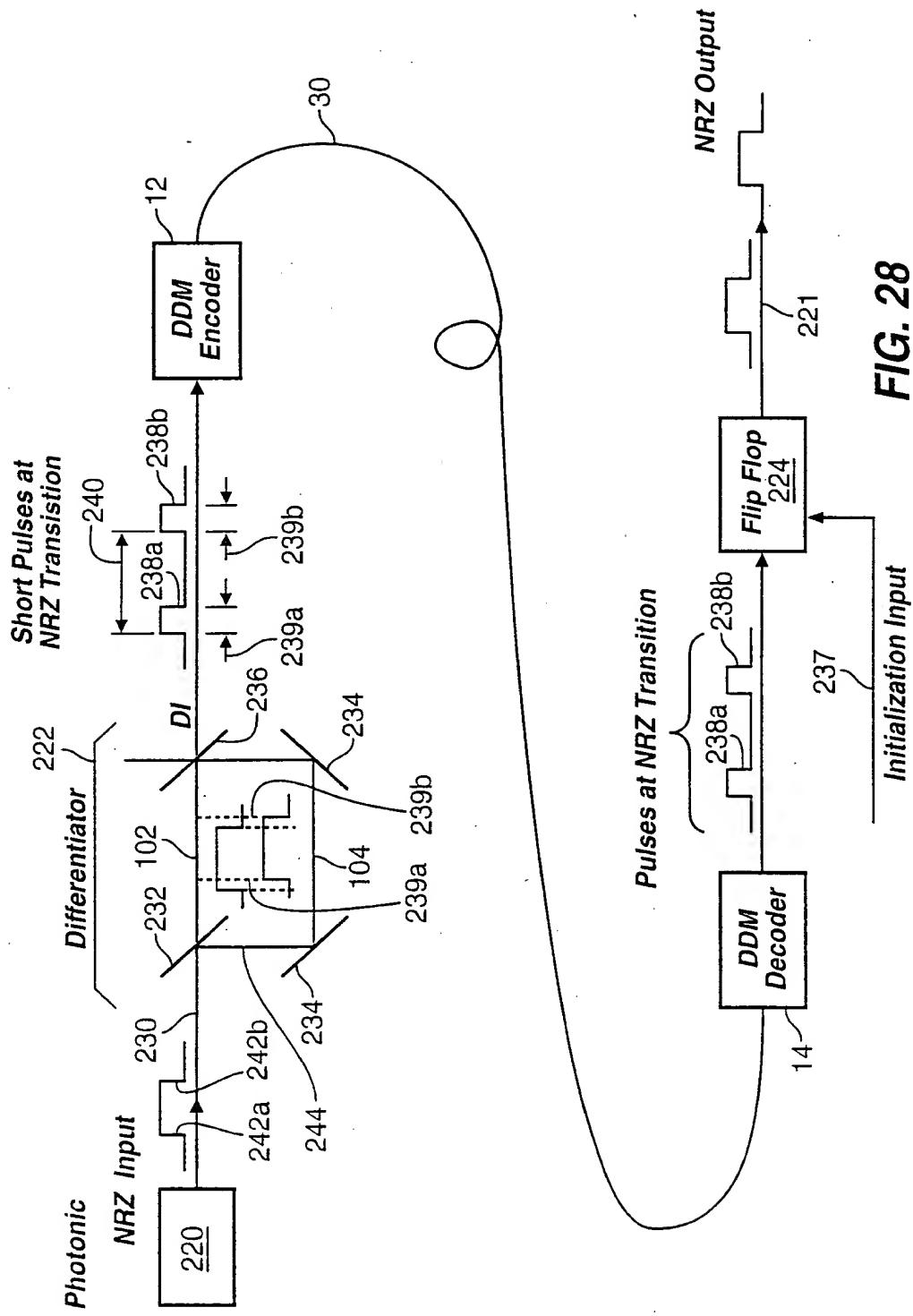


FIG. 28

*Initialization Input*

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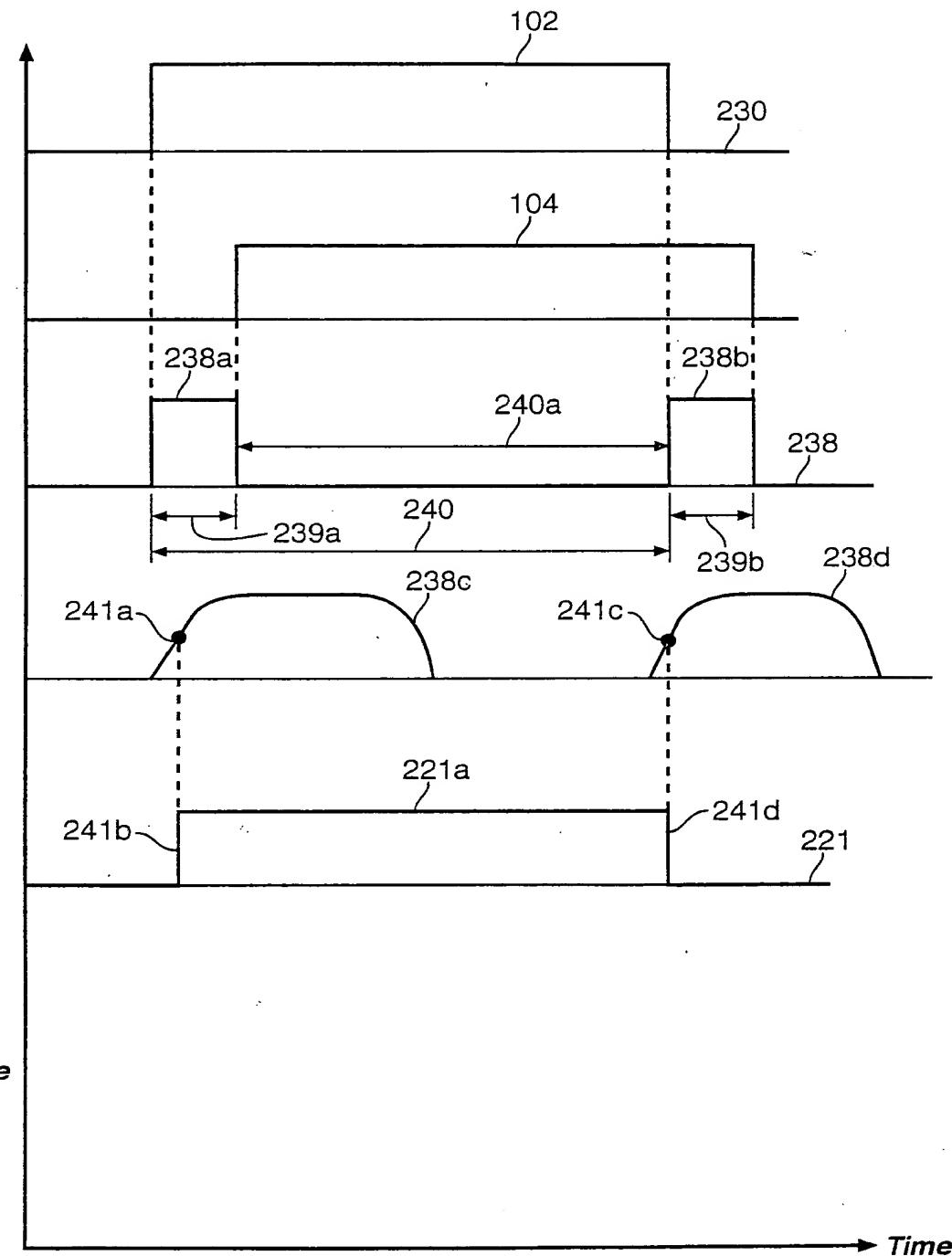
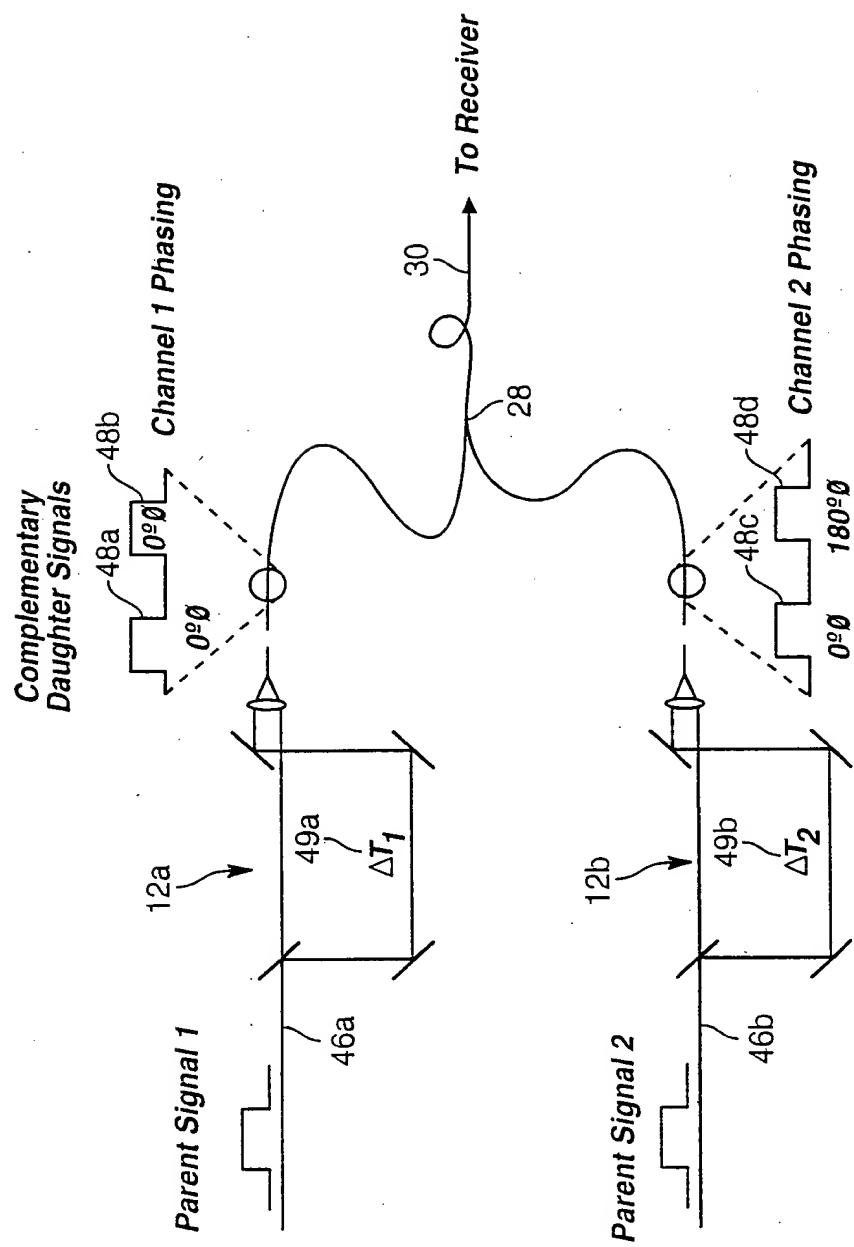


FIG. 29



Phase Sequenced Dual Channel Encoder

FIG. 30

## Phase Sequenced Dual Channel Decoder

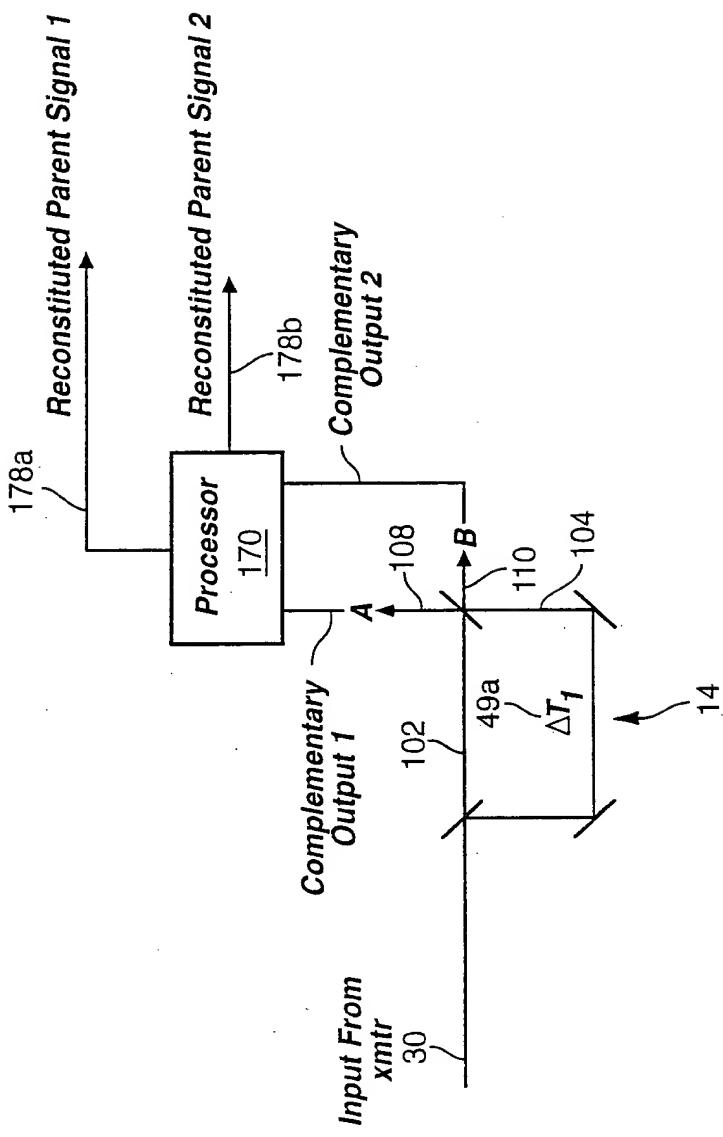


FIG. 31

## Phase Sequence Timing

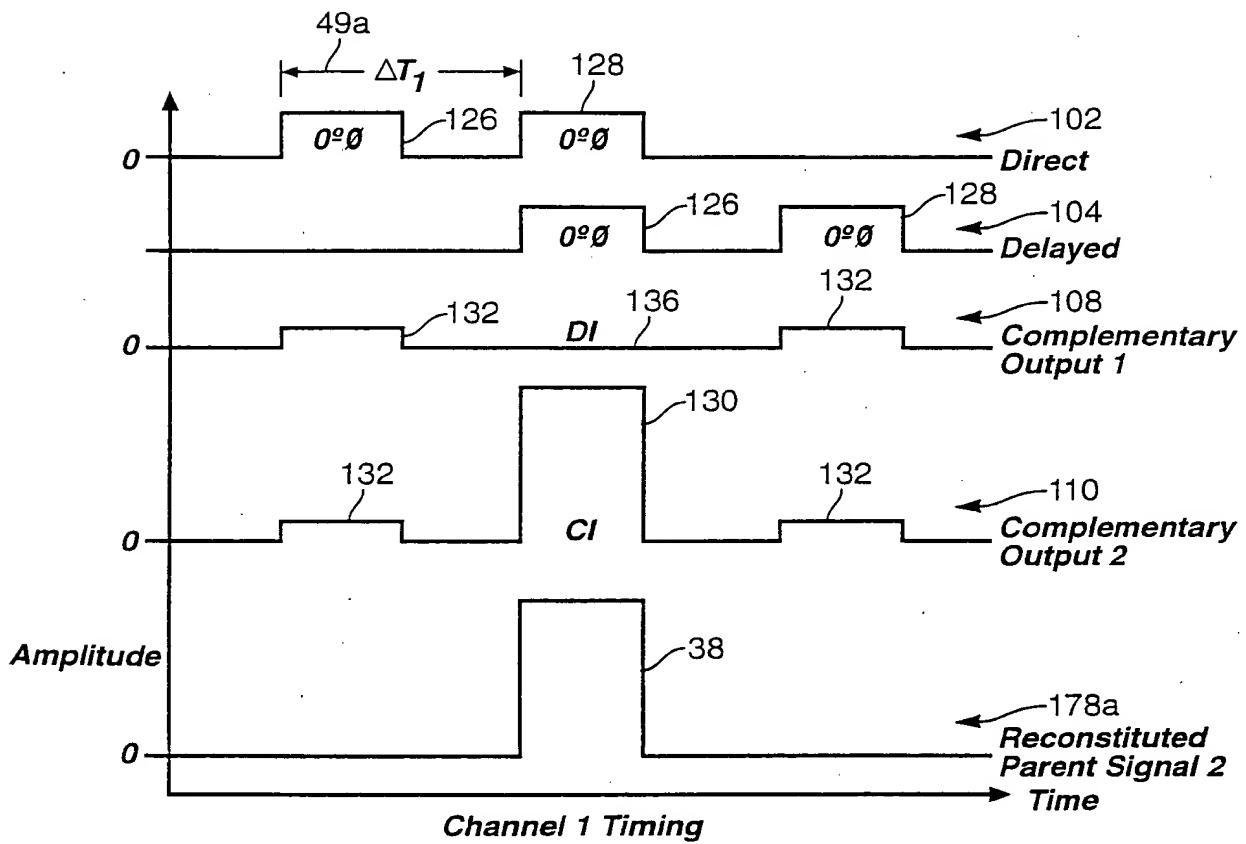


FIG. 32

## Phase Sequence Timing

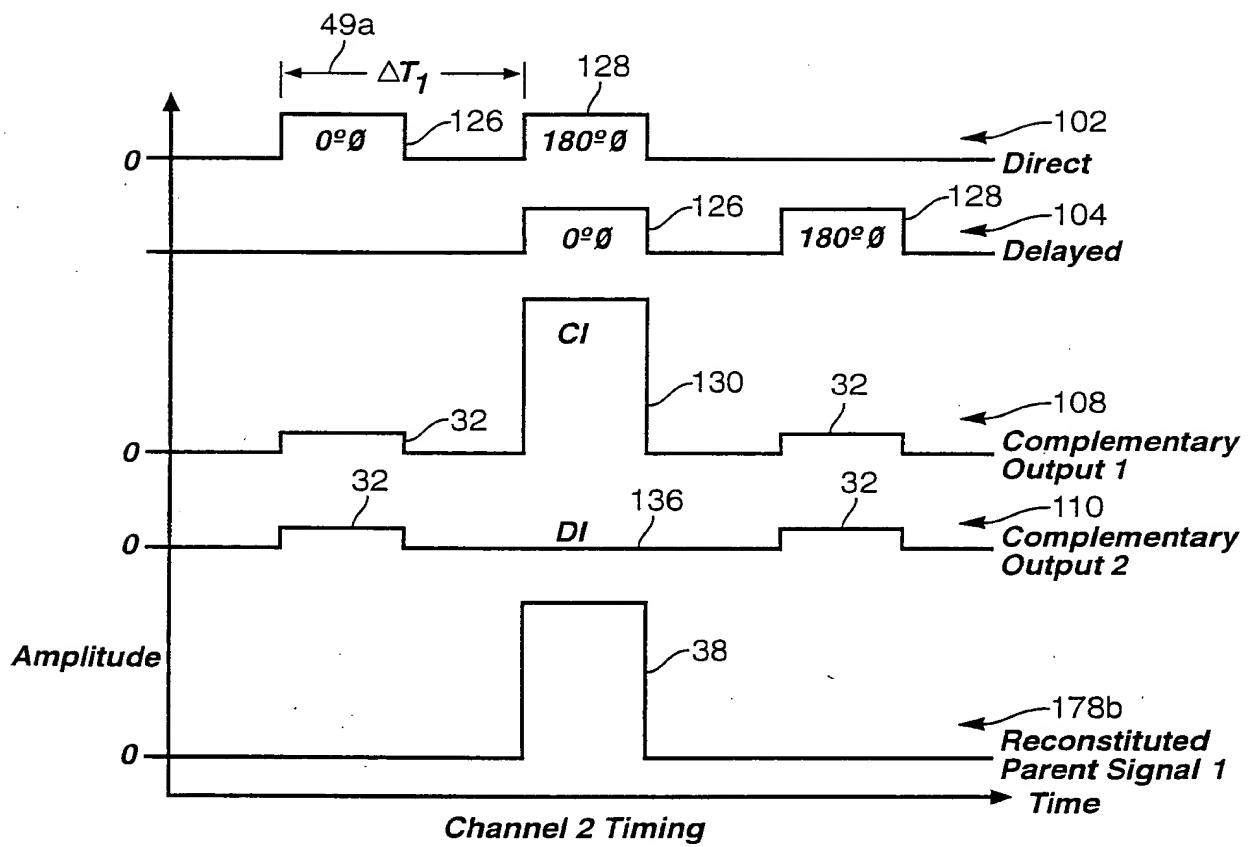


FIG. 33

### Quadrature Encoding/Decoding Apparatus

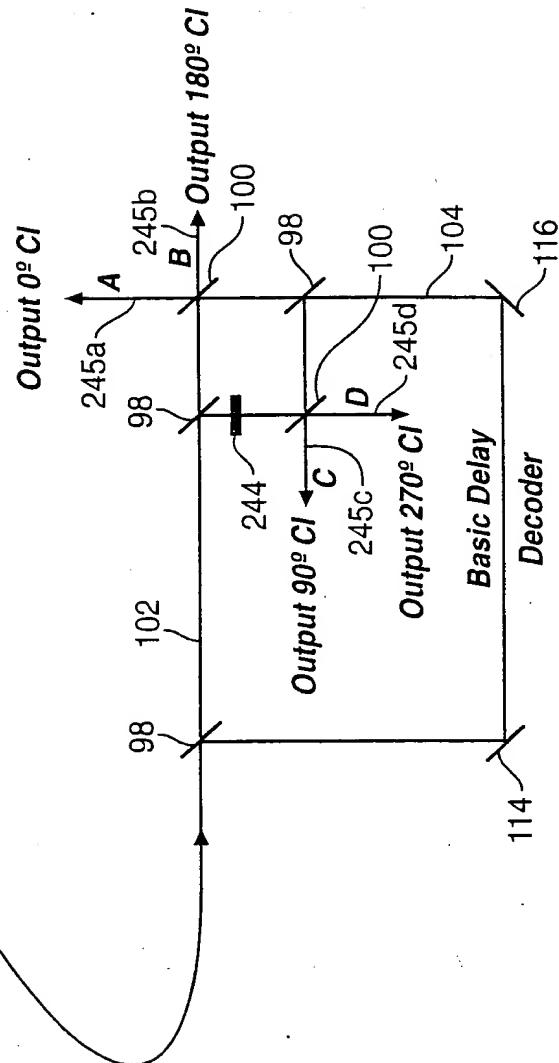
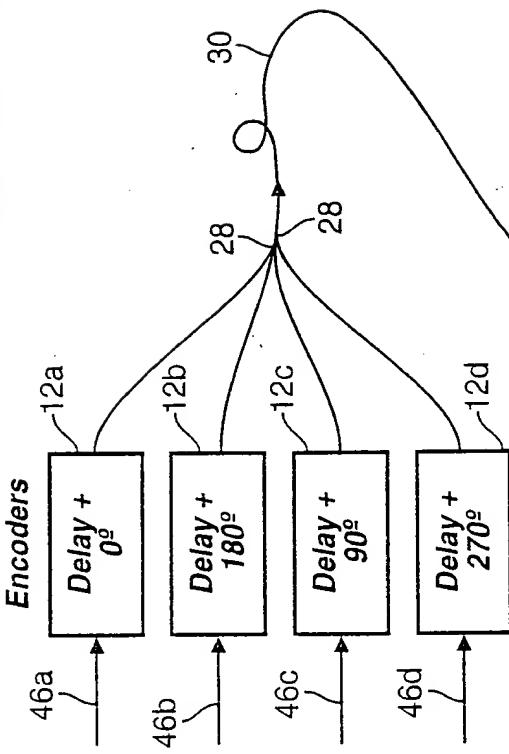


FIG. 34

Diagram illustrating the relationship between four channels (46a, 46b, 46c, 46d) and the resulting quadrature outputs (A, B, C, D) based on the phase of direct and delayed signals.

The table shows the following data:

<i>Phase of Direct Signal</i>	<i>Phase of Delayed Signal</i>	<i>Quadrature Outputs</i>			
		<i>A</i>	<i>B</i>	<i>C</i>	<i>D</i>
0	0	<i>Cl</i>	<i>DI</i>	<i>C</i> = <i>D</i>	
0	180	<i>DI</i>	<i>Cl</i>	<i>C</i> = <i>D</i>	
0	90	<i>A</i> = <i>B</i>	<i>Cl</i>	<i>DI</i>	
0	270	<i>A</i> = <i>B</i>	<i>DI</i>	<i>Cl</i>	

Arrows point from the table to four points (245a, 245b, 245c, 245d) below it.

FIG. 35

## Quadrature Wave Forms For One Channel

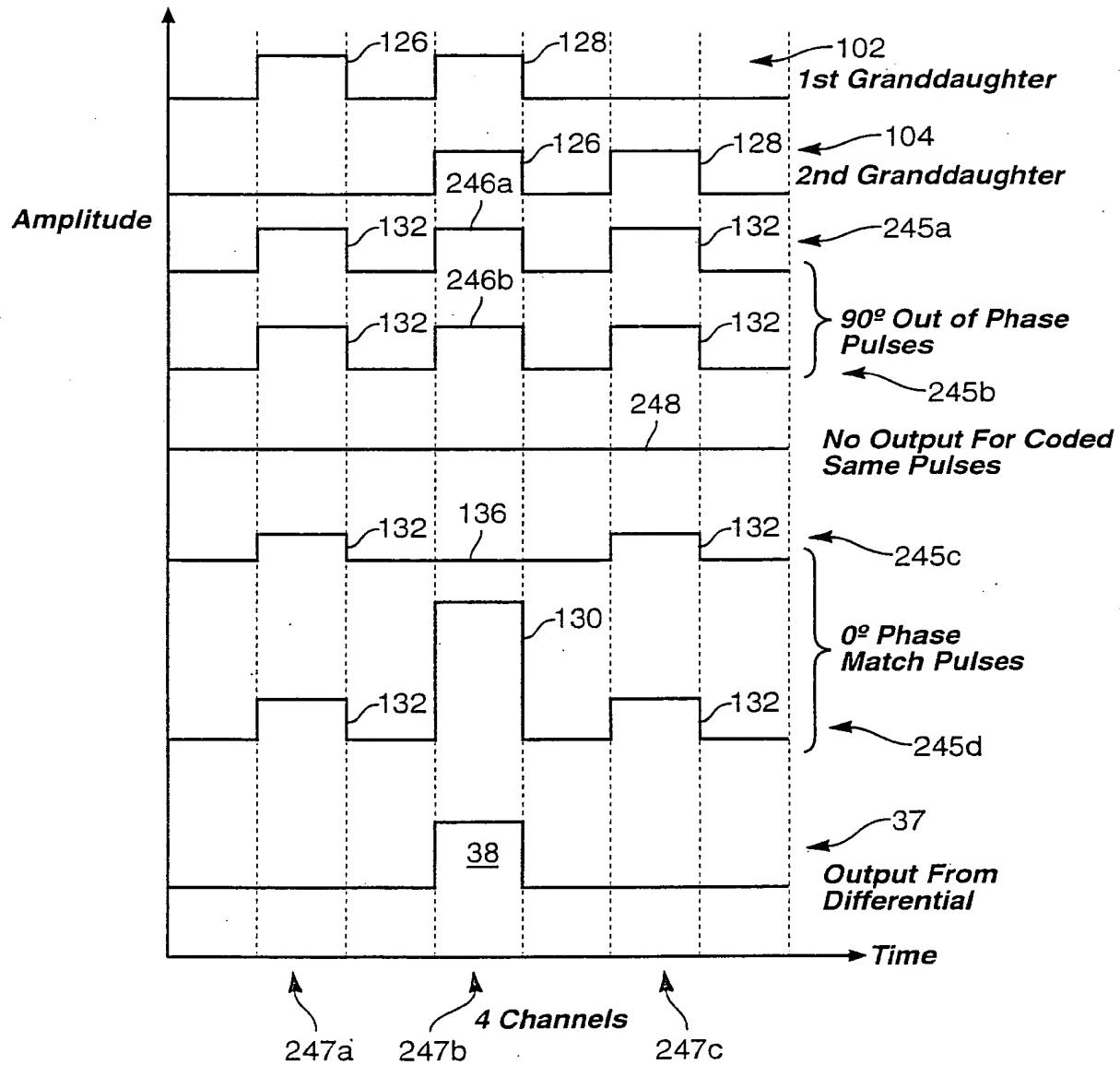
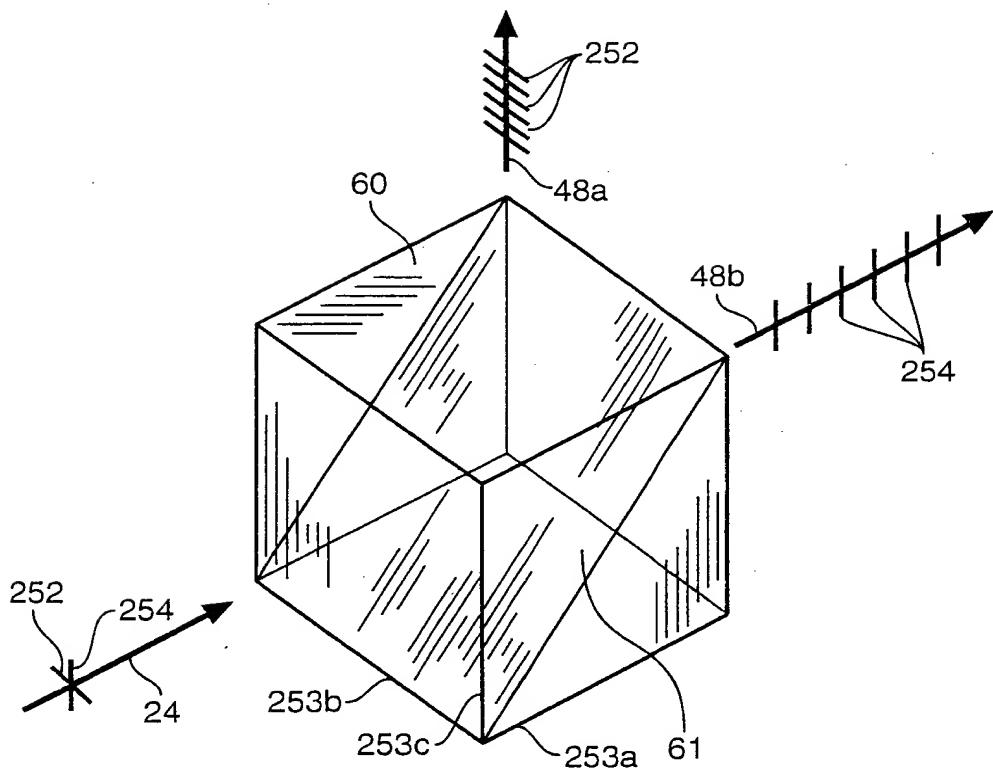


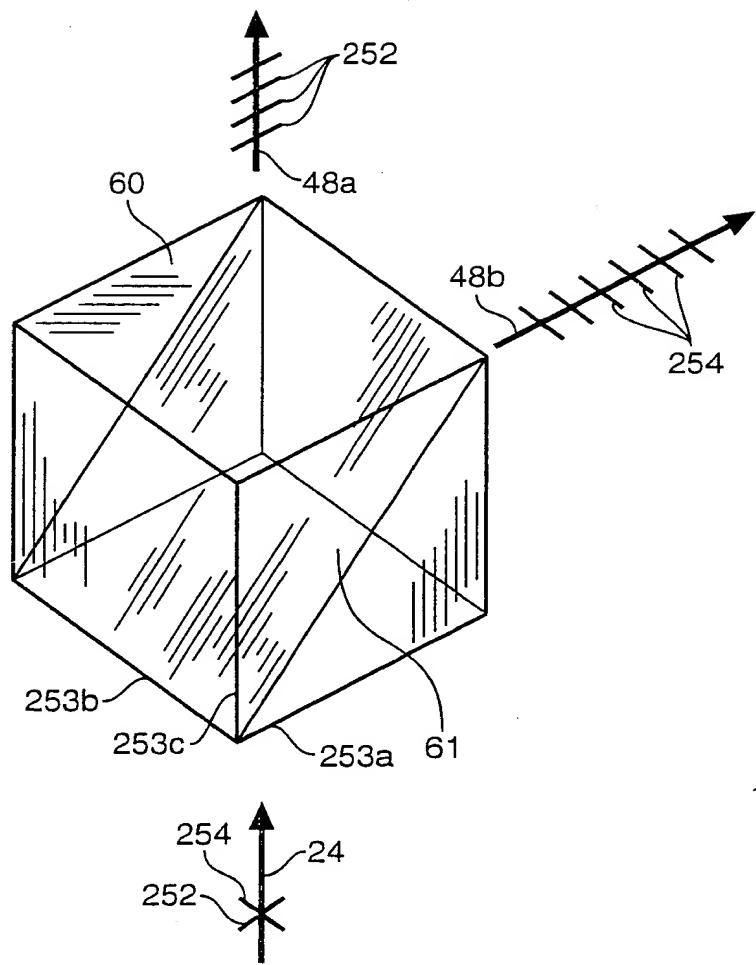
FIG. 36

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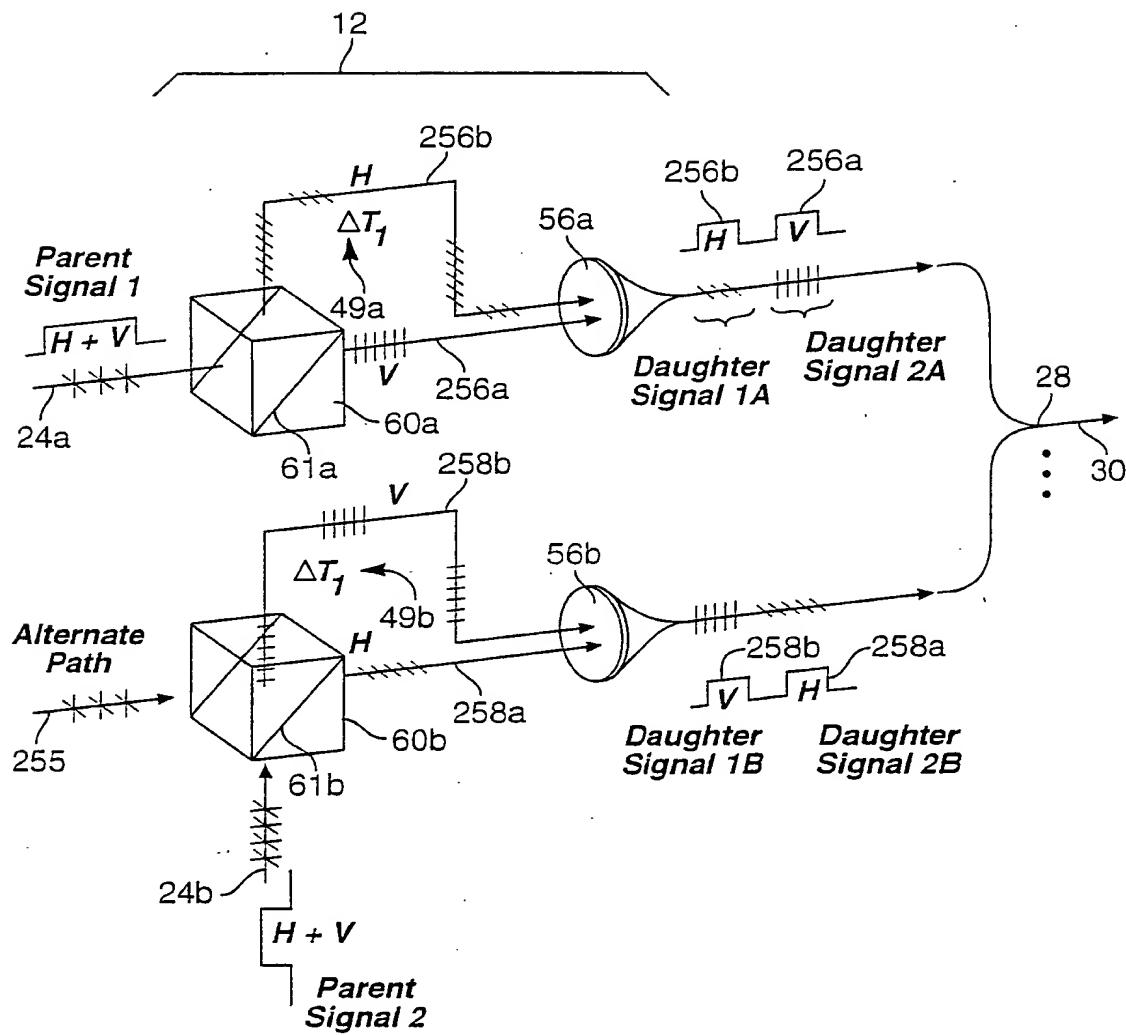


**FIG. 37A**

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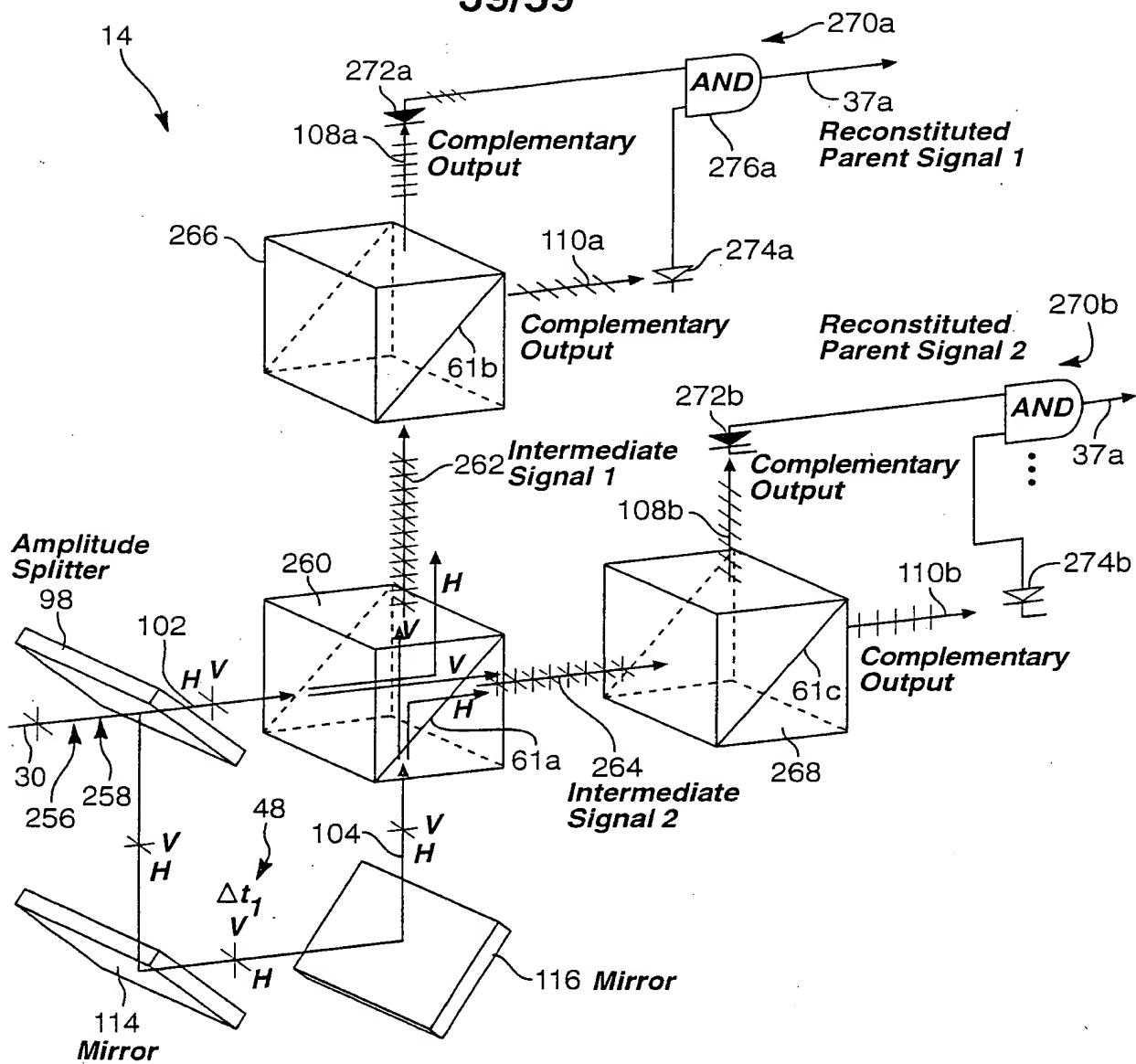


**FIG. 37B**



**Double Encoder With Polarizations Sequenced to Differentiate 2 Channels Having the Same Time Delay Between Daughter Signals**

FIG. 38



**Double Decoder With Polarizations Sequenced to Differentiate 2 Channels Having the Same Time Delay Between Daughter Signals**

**FIG. 39**

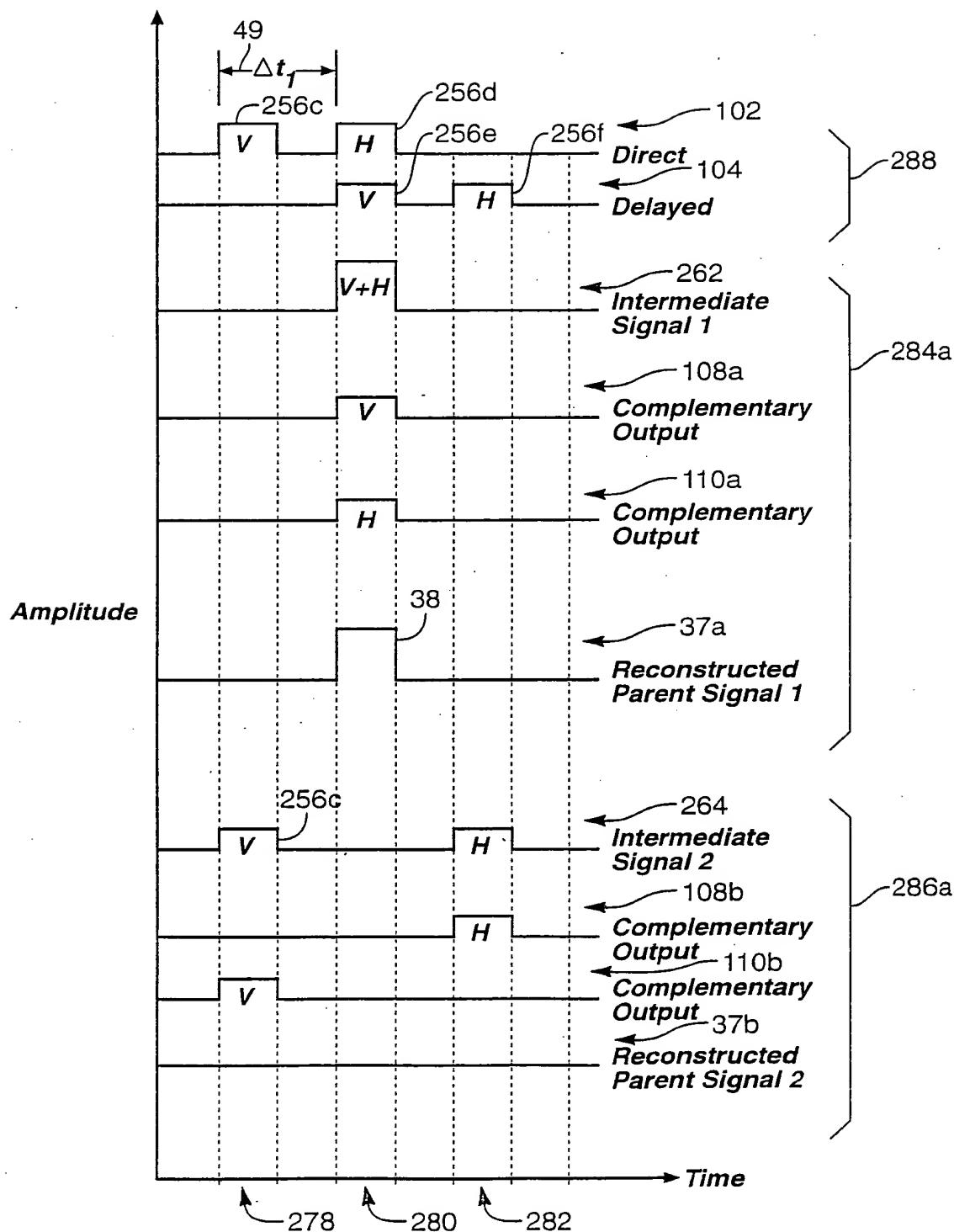


FIG. 40

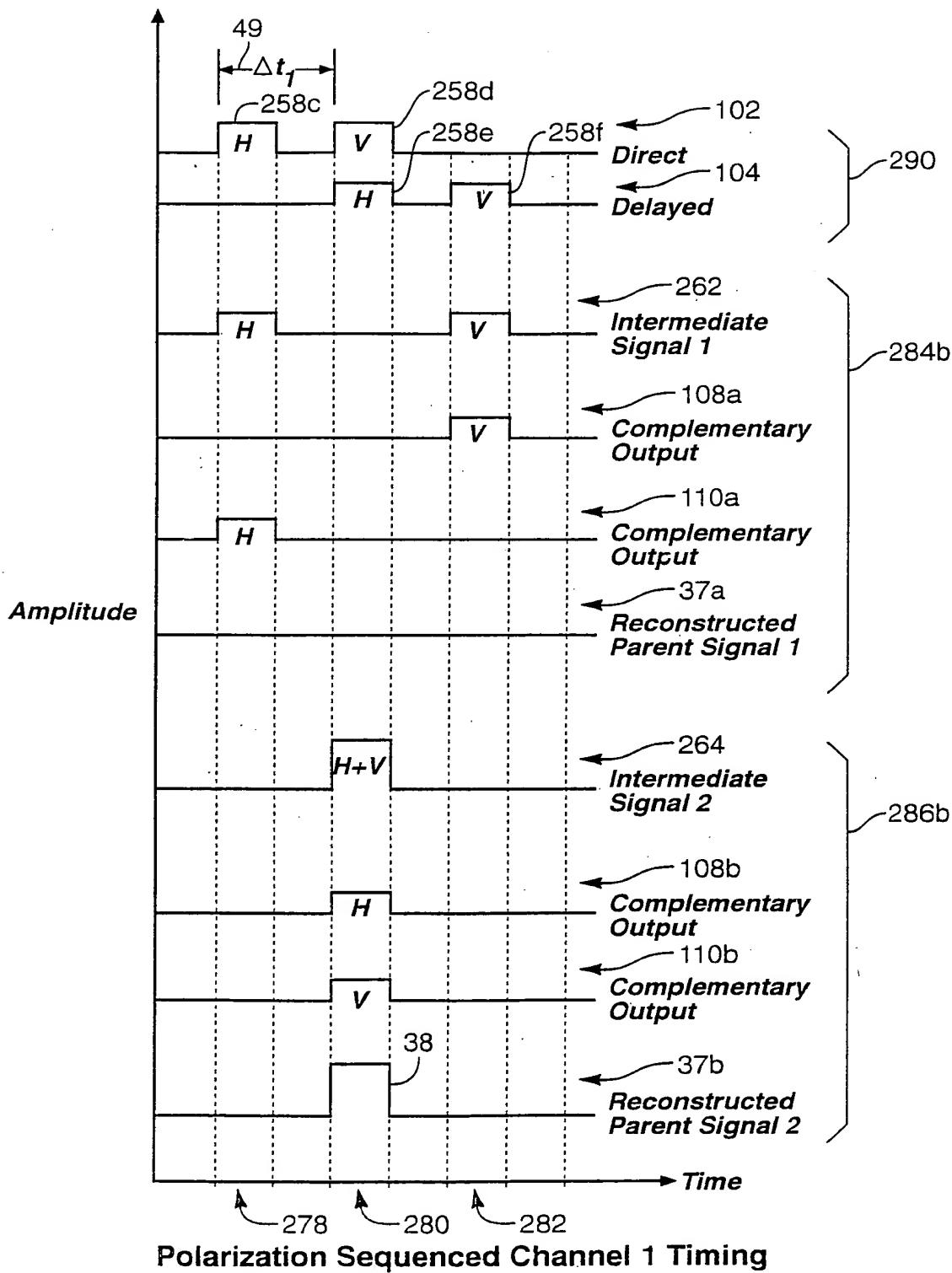


FIG. 41

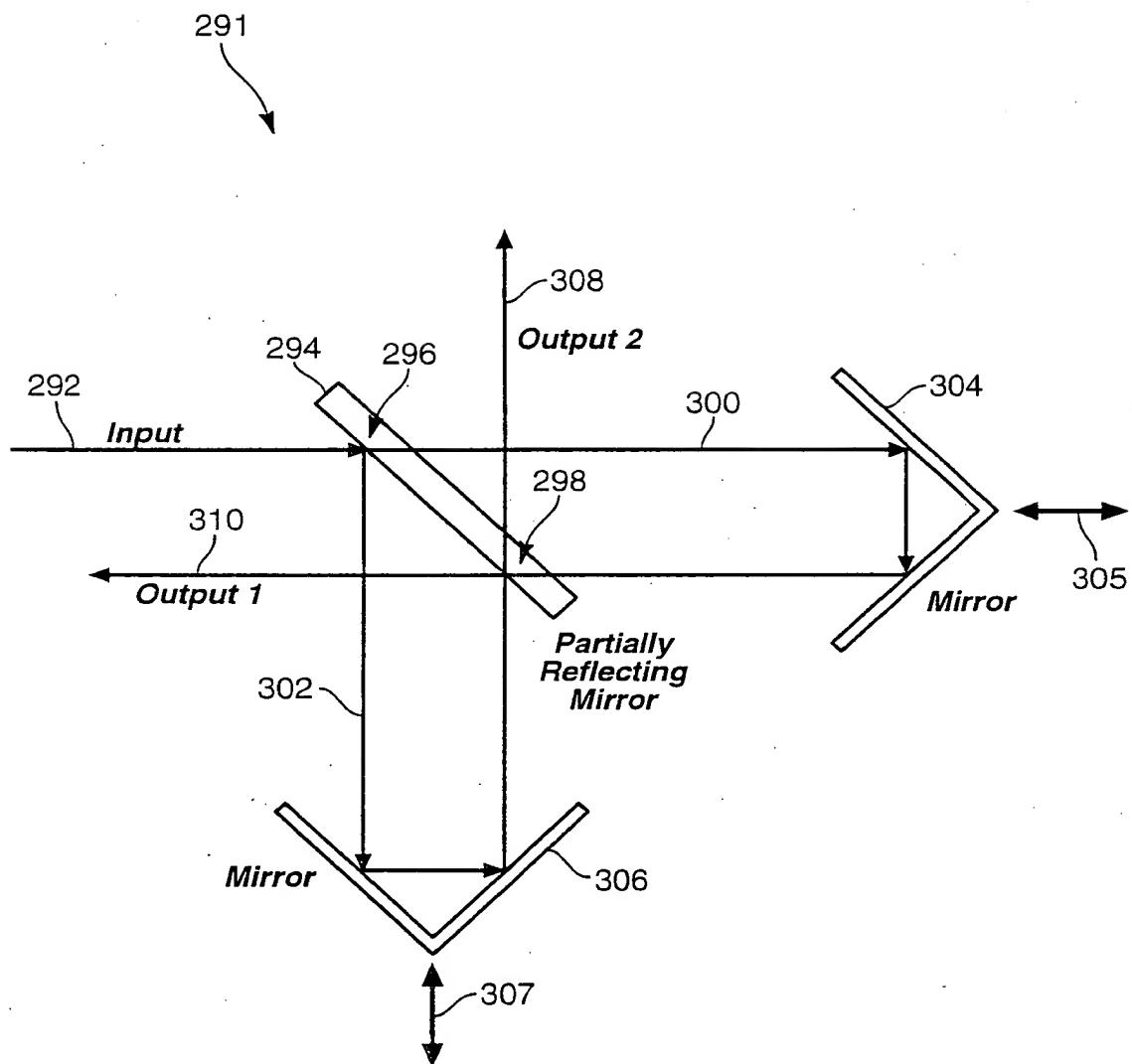


FIG. 42

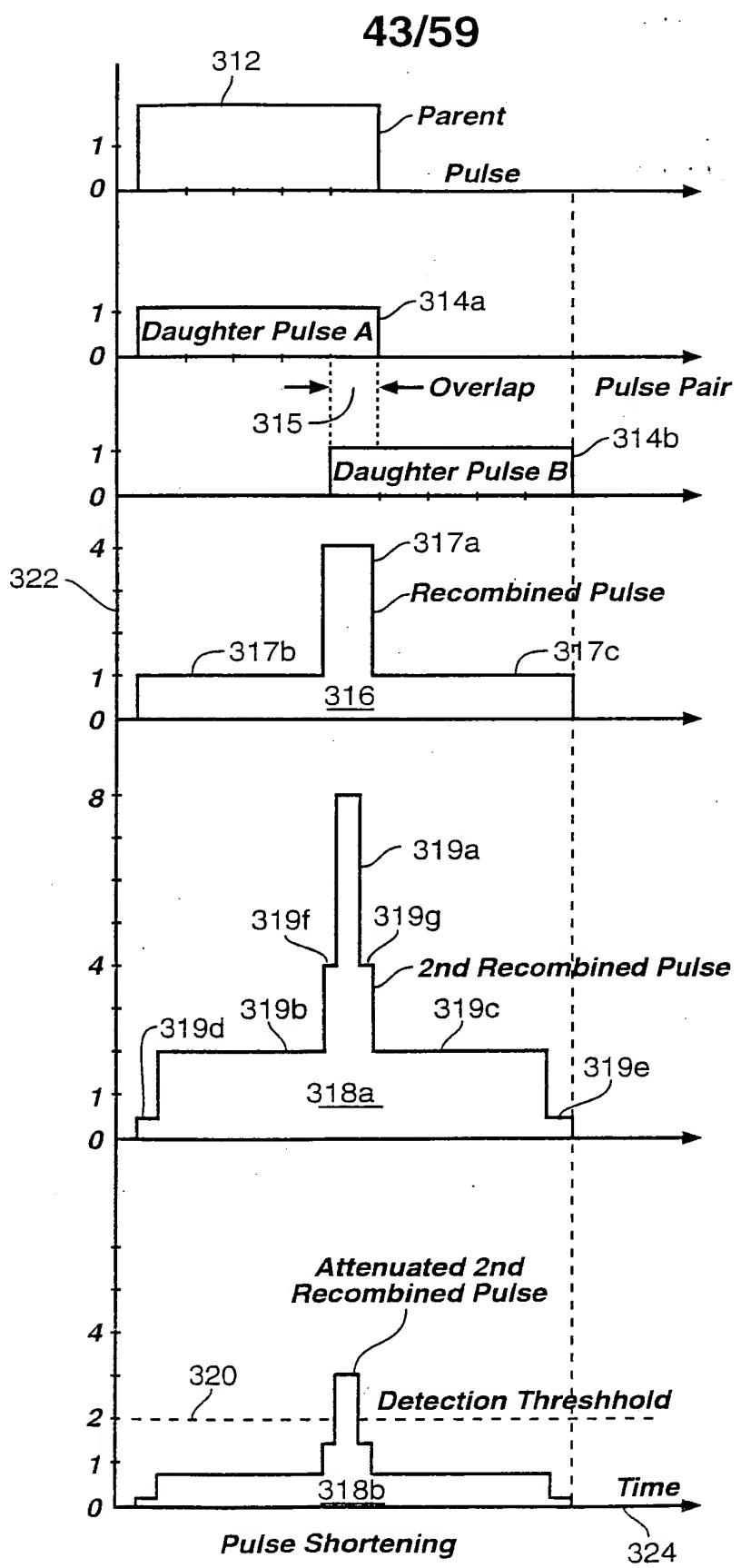


FIG. 43

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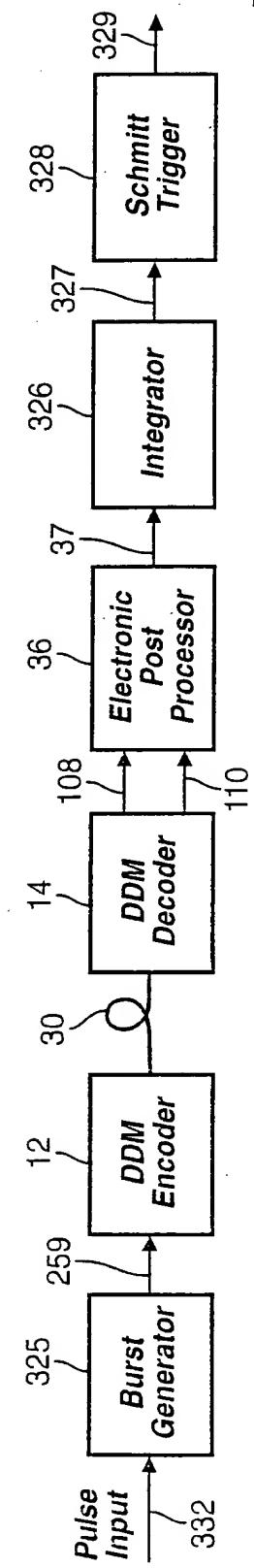


FIG. 44

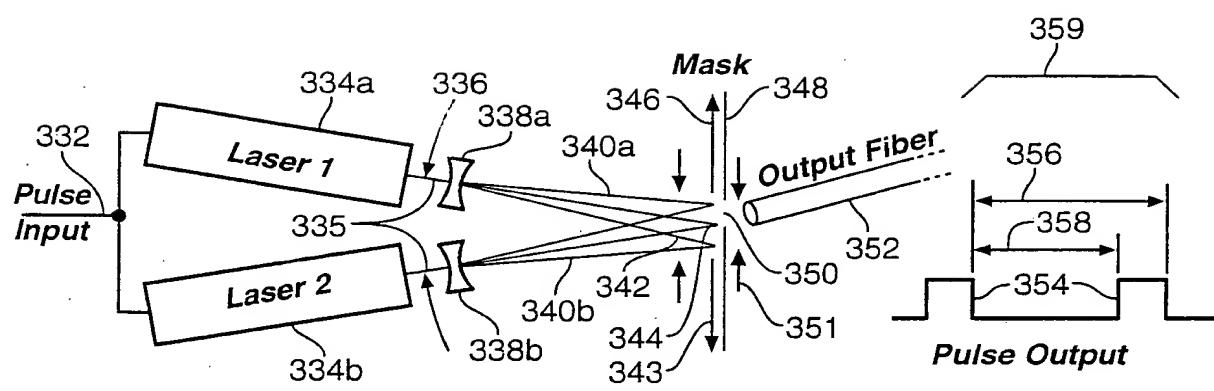


FIG. 45

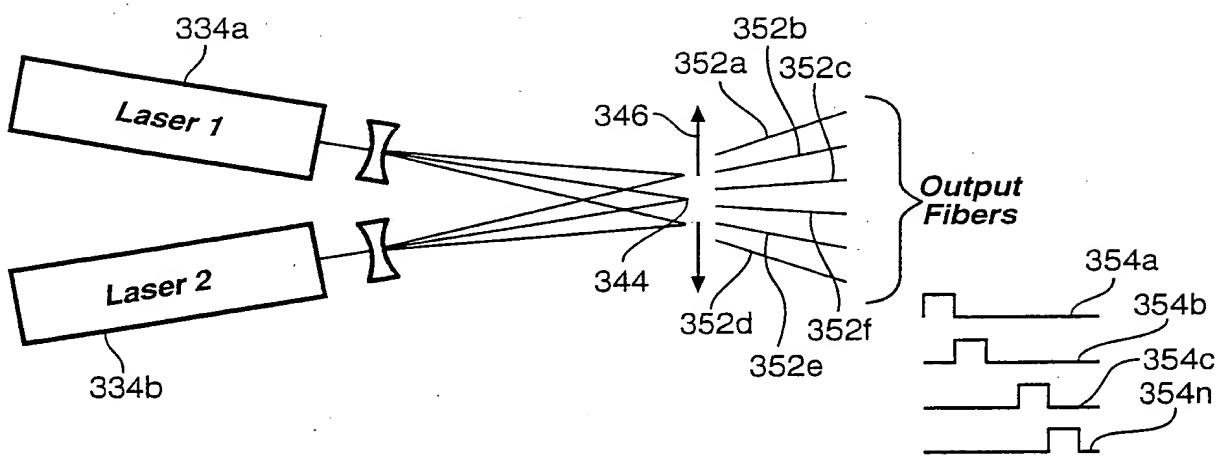
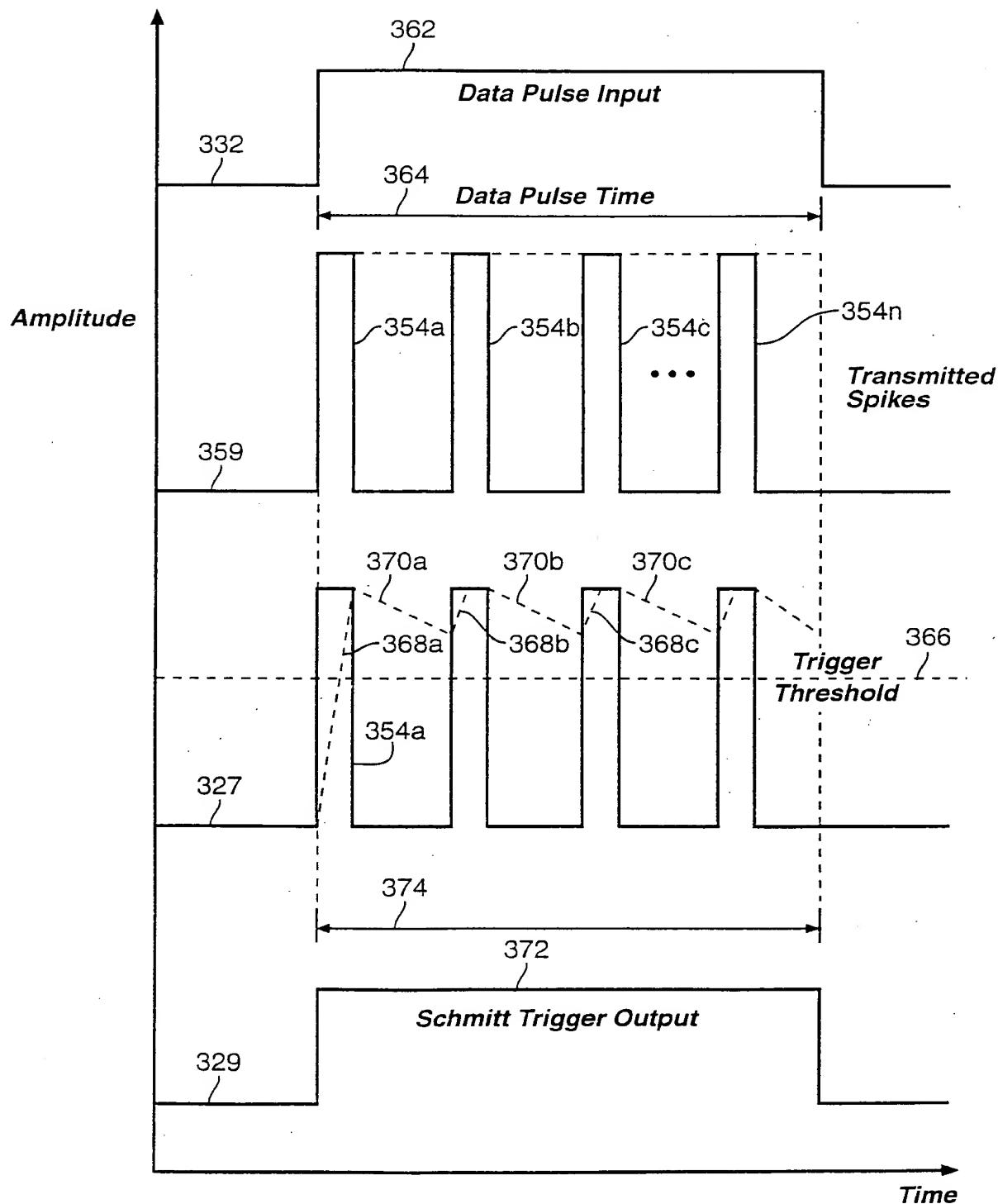


FIG. 46

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**FIG. 47**

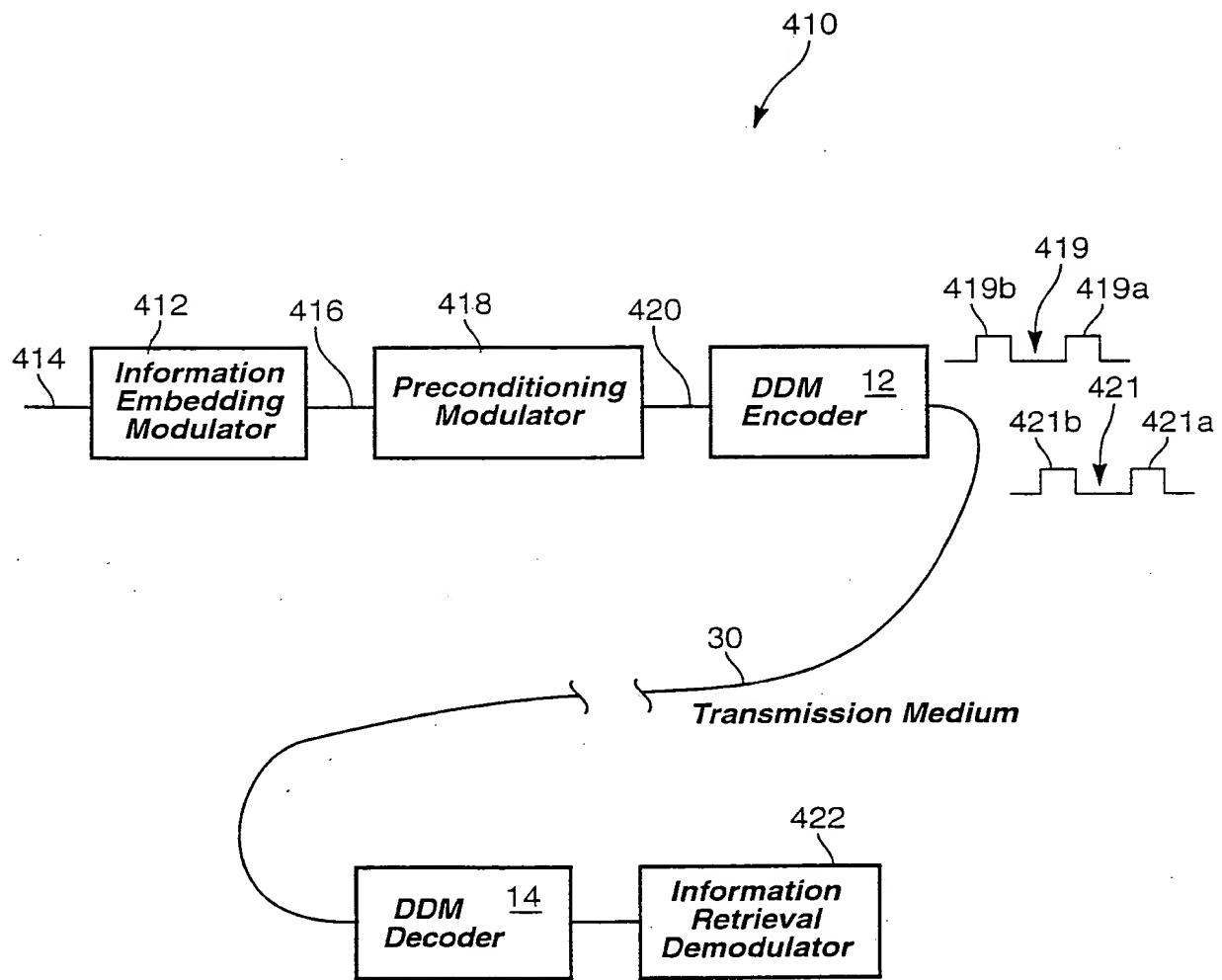


FIG. 48

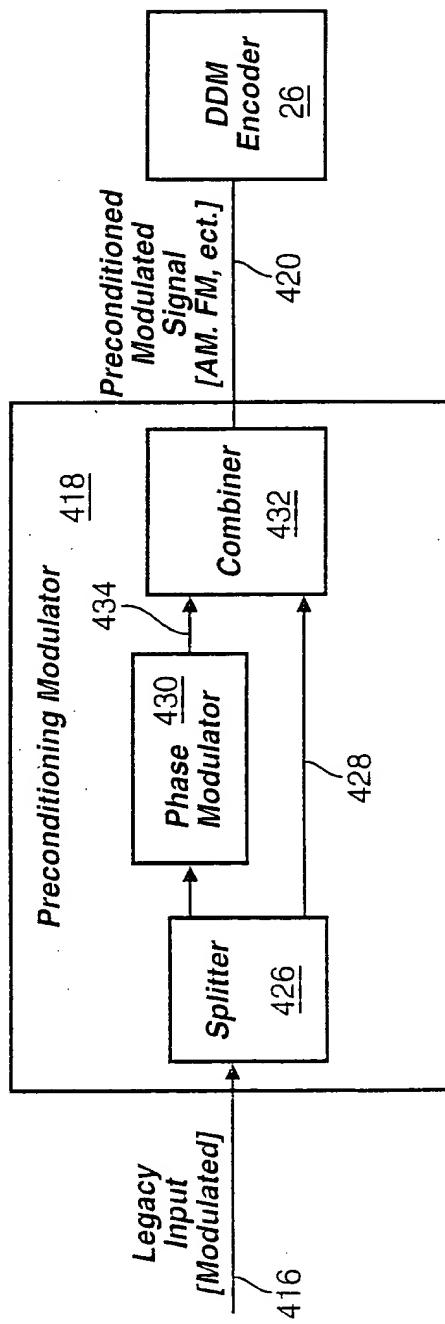


FIG. 49

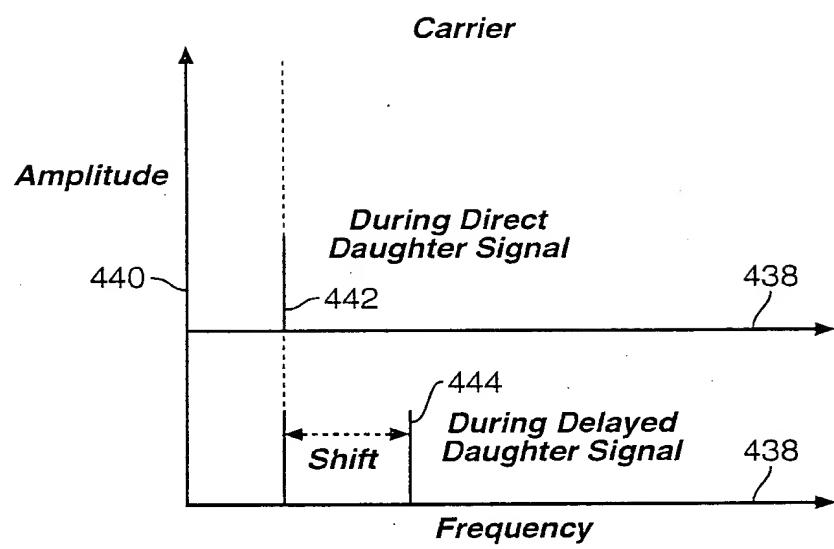


FIG. 50

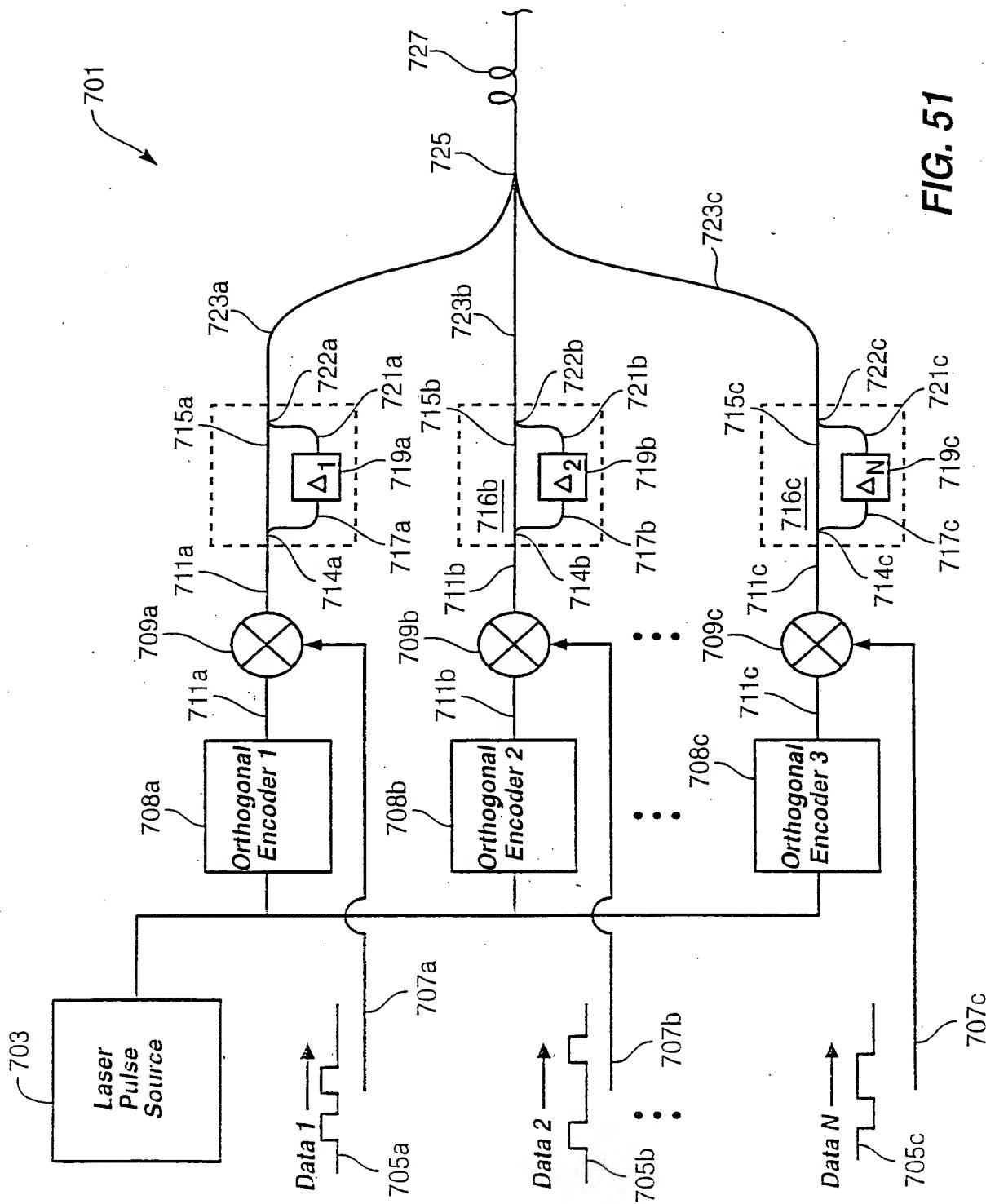


FIG. 51

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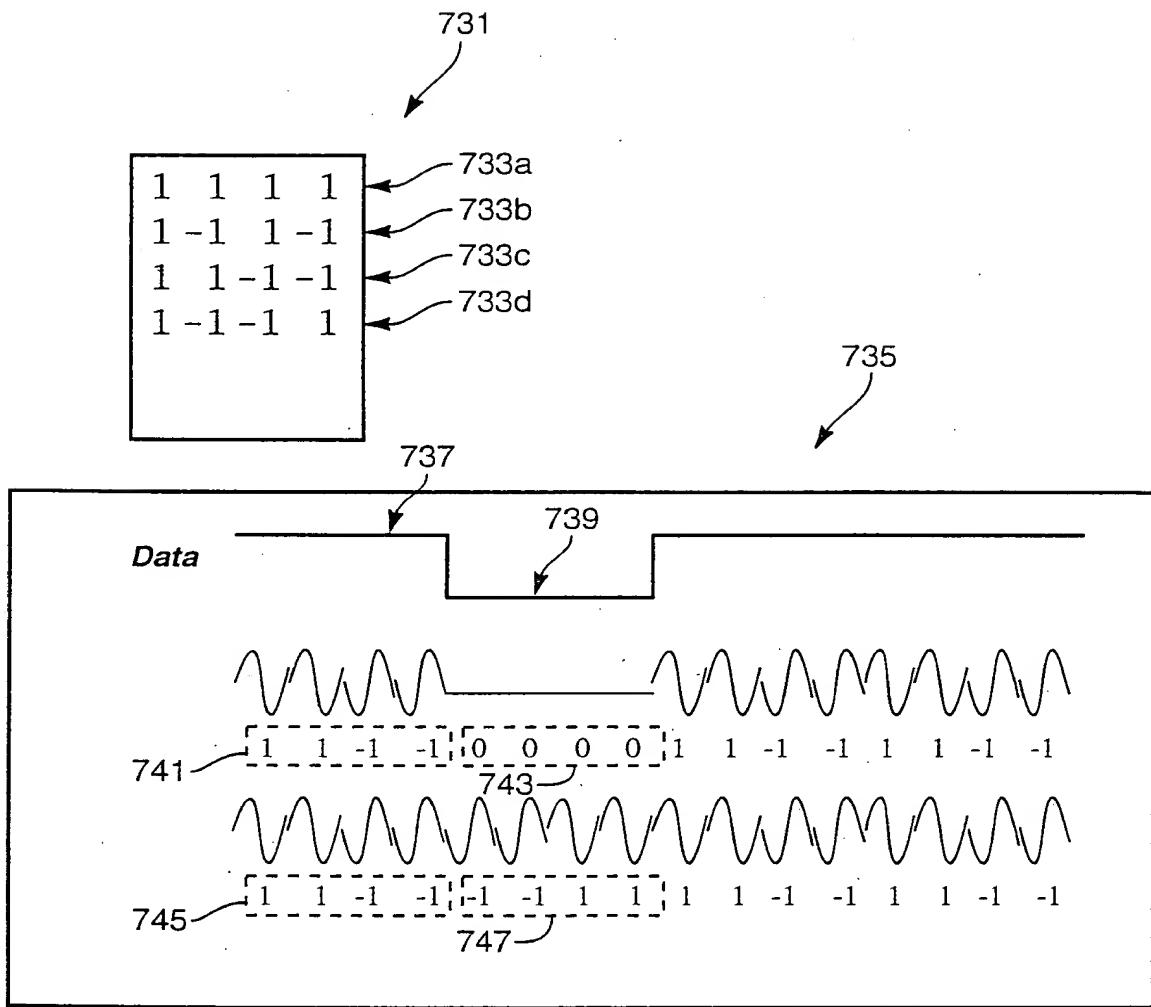


FIG. 52

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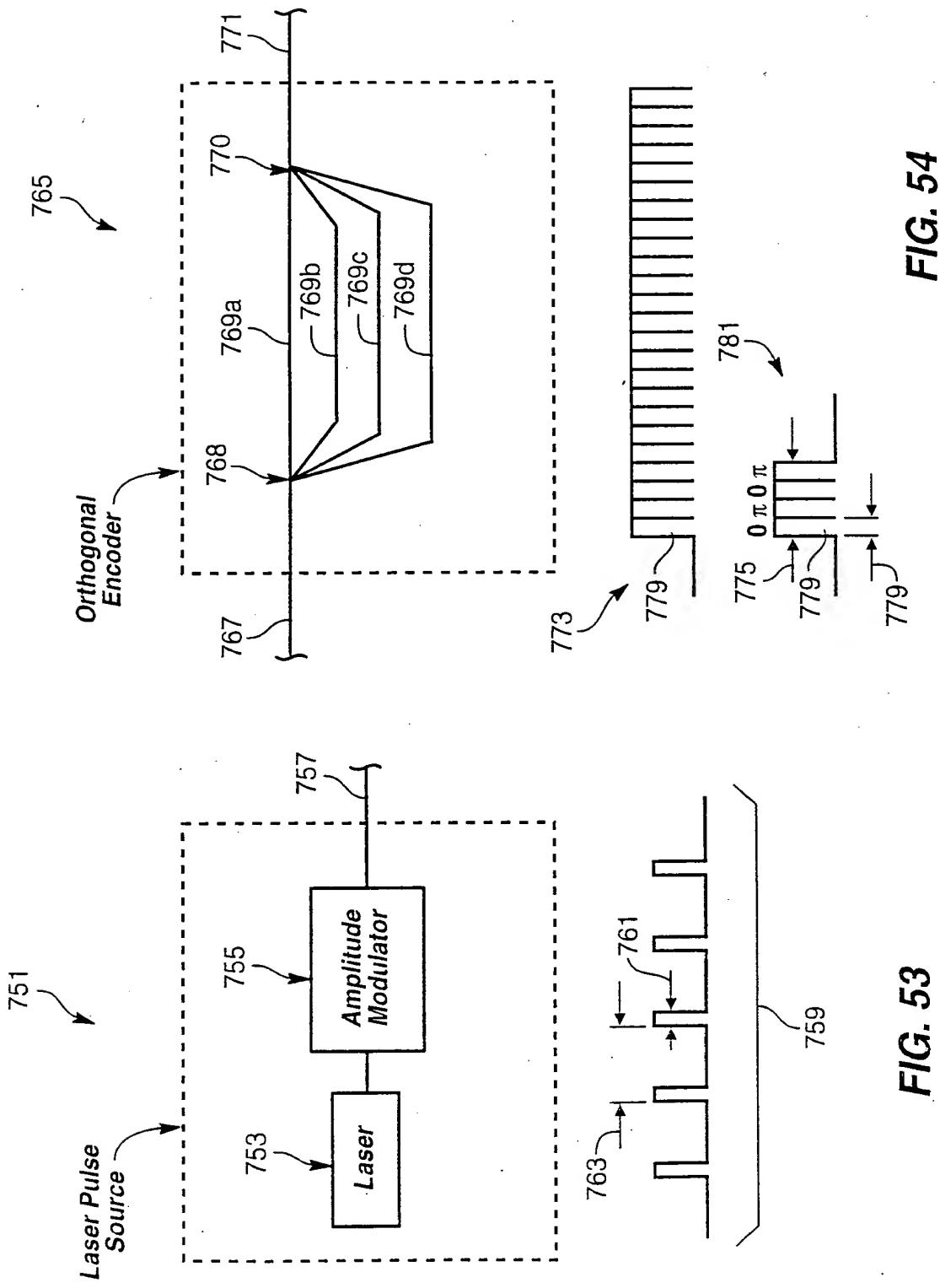
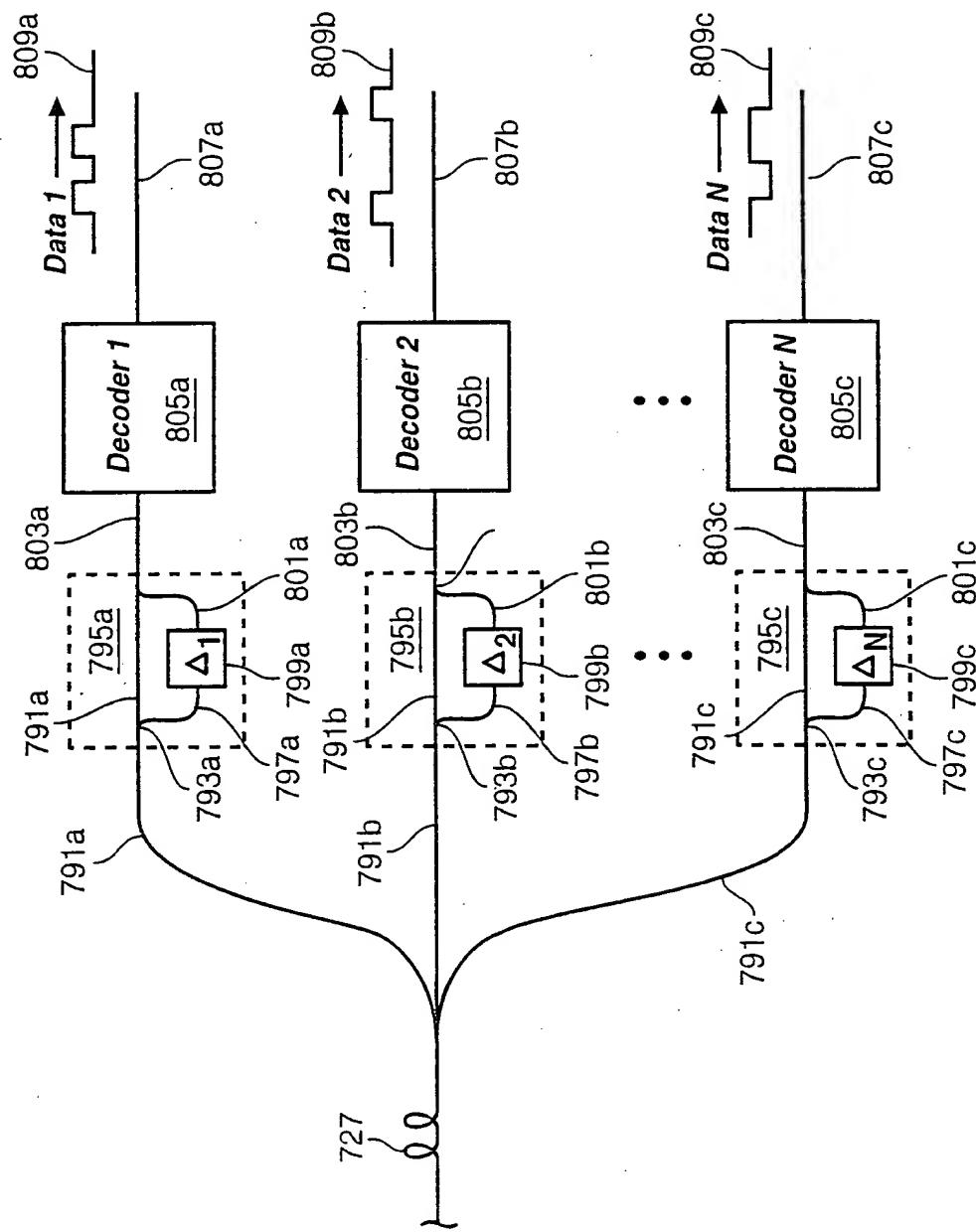


FIG. 53

FIG. 54



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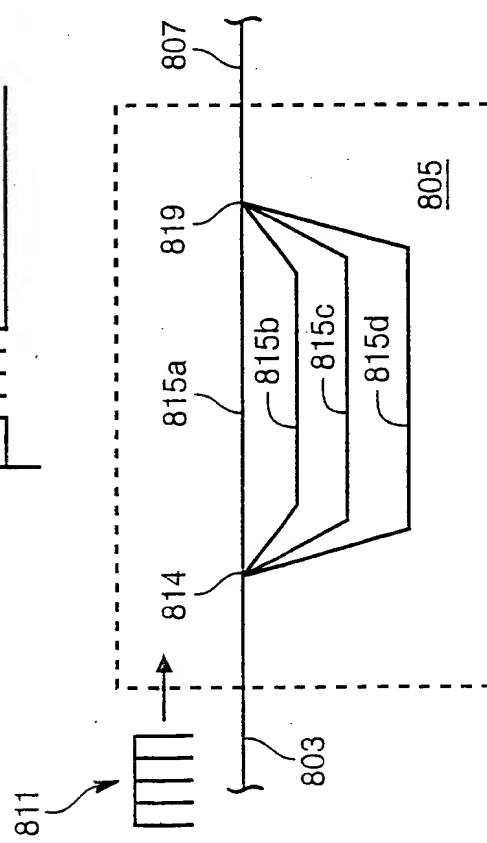
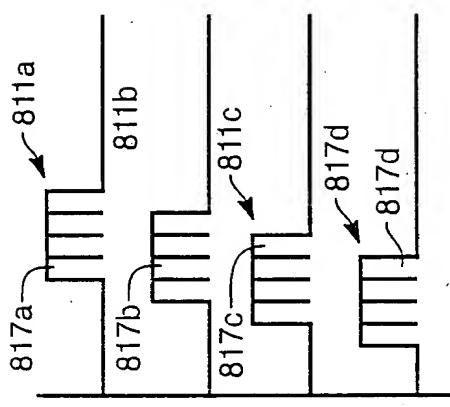


FIG. 56

FIG. 57

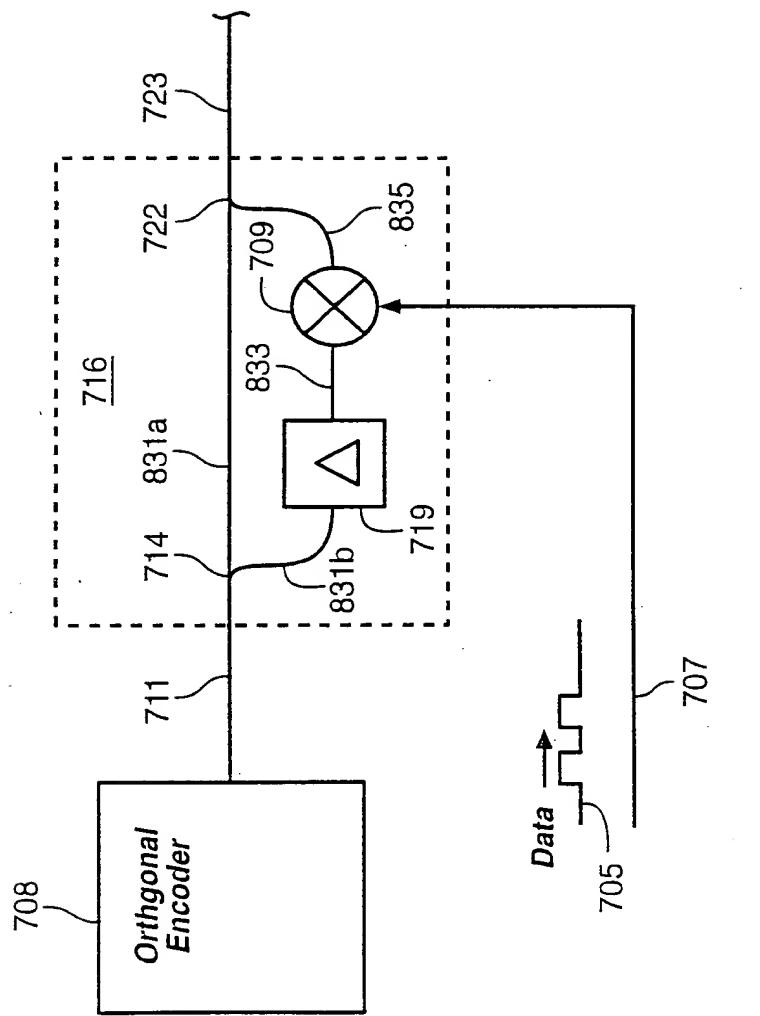


FIG. 58

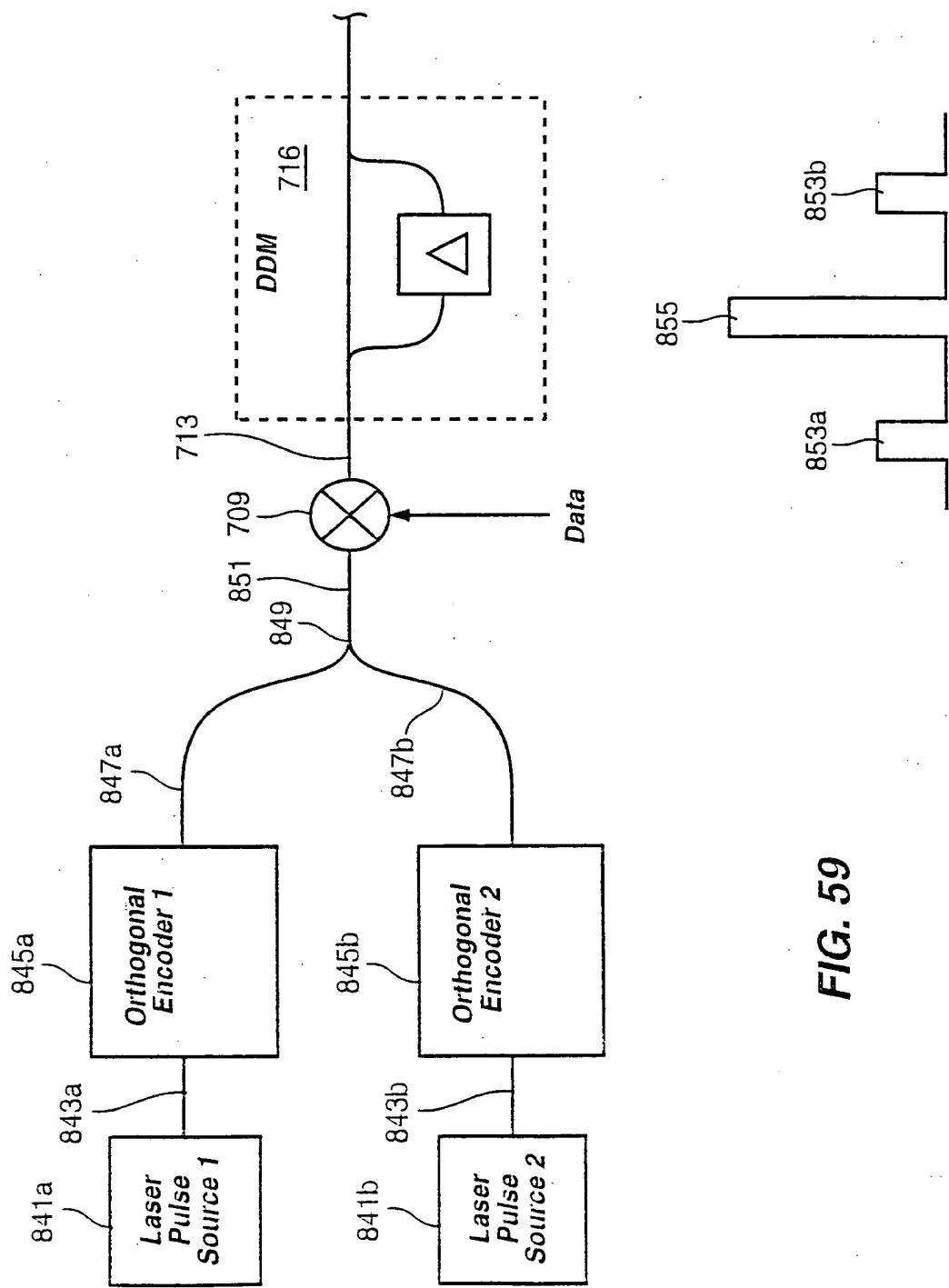


FIG. 59

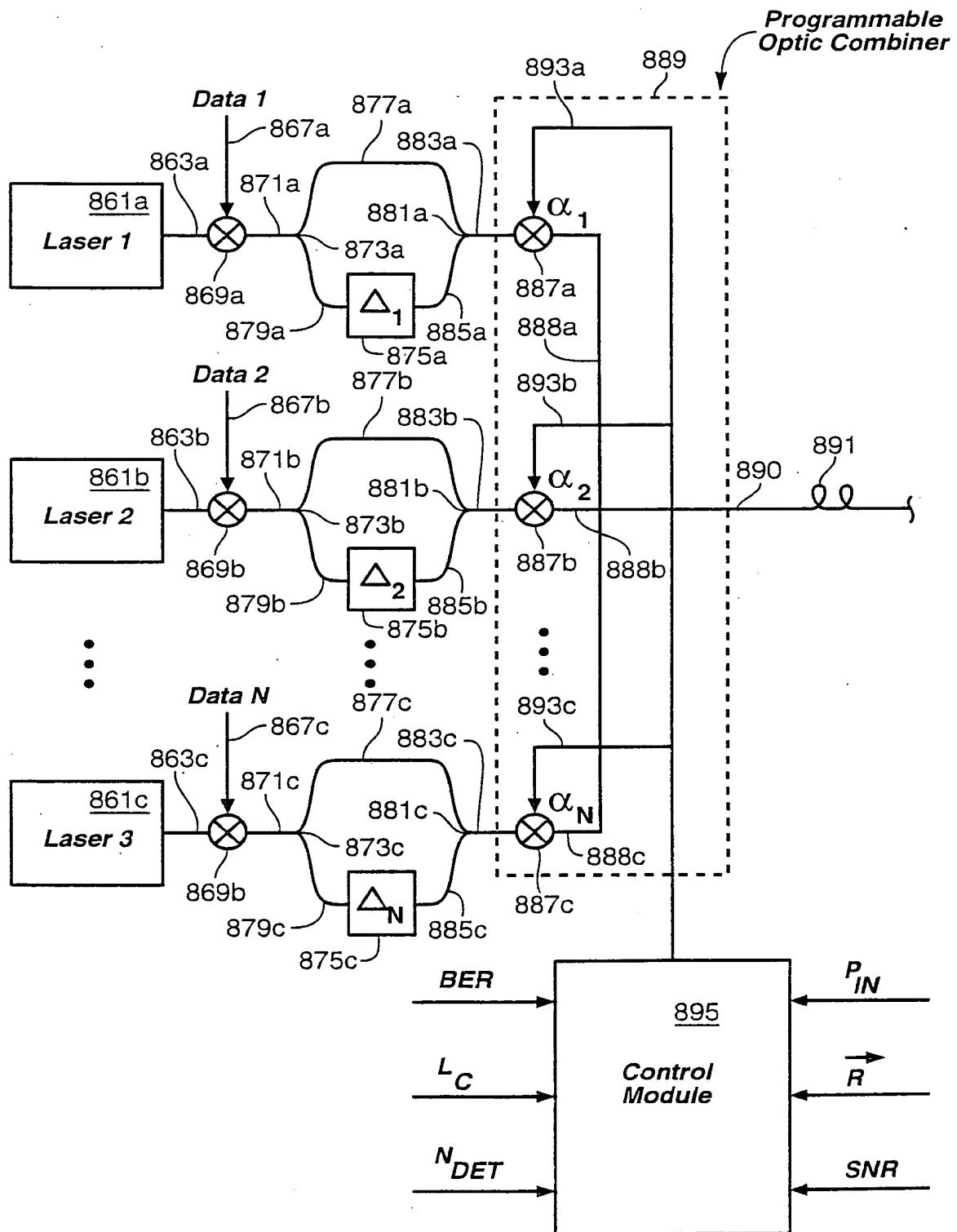


FIG. 60

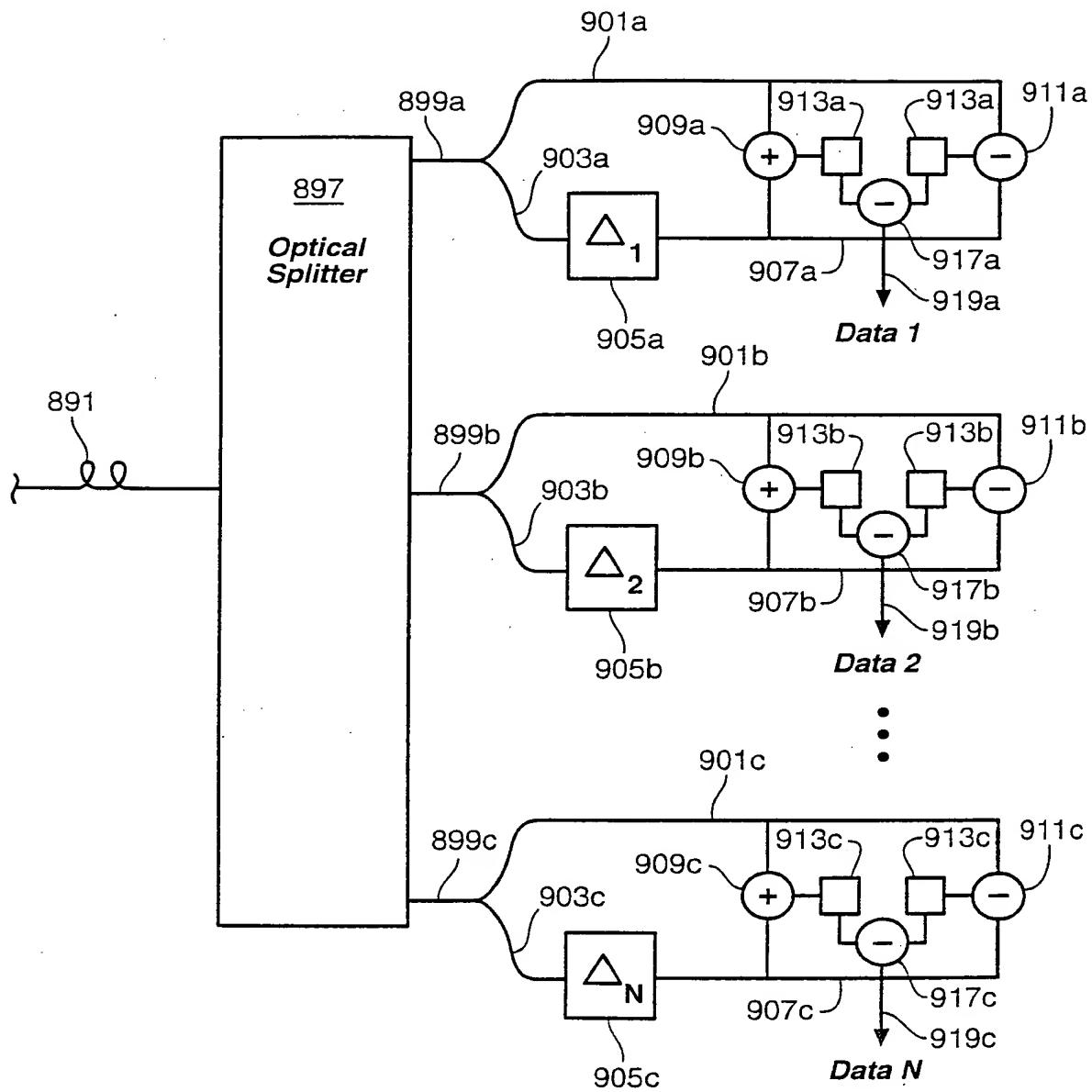


FIG. 61